Die Pad Number	Die Pad Name	Die Pad Upper Right X (microns)	Die Pad Upper Right Y (microns)	Die Pad Lower Left X (microns)	Die Pad Lower Left Y (microns)	Chip Pkg. Pin Number	Chip Pkg. Pin Name	X Length	Ylength	X center	Ycenter	Translated Origin X center	Translated Origin Ycenter	X Pitch	Y Pitch
1	man_1_p<4>	3835.6	3437.3	3745.6	3347.3	man_1_p<4>		90	90	3790.6	3392.3	**3740.6**	**3342.3**		
	man_1_n<4>	3715.6	3437.3	3625.6	3347.3	man_1_n<4>		90	90	3670.6	3392.3	**3620.6**	**3342.3**	-120	0
	capid_1_p<0>	3595.6	3437.3	3505.6	3347.3	capid_1_p<0>		90	90	3550.6	3392.3	**3500.6**	**3342.3**	-120	0
	capid_1_n<0>	3475.6	3437.3	3385.6	3347.3	capid_1_n<0>		90	90	3430.6	3392.3	**3380.6**	**3342.3**	-120	0
	capid_1_p<1>	3355.6	3437.3	3265.6	3347.3	capid_1_p<1>		90	90	3310.6	3392.3	**3260.6**	**3342.3**	-120	0
	capid_1_n<1>	3235.6	3437.3	3145.6	3347.3	capid_1_n<1>		90	90	3190.6	3392.3	**3140.6**	**3342.3**	-120	0
	vdd	3115.6	3437.3	3025.6	3347.3	vdd		90	90	3070.6	3392.3	**3020.6**	**3342.3**	-120	0
	gnd	2995.6	3437.3	2905.6	3347.3	gnd		90	90	2950.6	3392.3	**2900.6**	**3342.3**	-120	0
	ck_1_p_qie	2875.6	3437.3	2785.6	3347.3	ck_1_p_qie		90	90	2830.6	3392.3	**2780.6**	**3342.3**	-120	0
	ck_1_n_qie	2755.6	3437.3	2665.6	3347.3	ck_1_n_qie		90	90	2710.6	3392.3	**2660.6**	**3342.3**	-120	0
	ck_0_p_qie	2635.6	3437.3	2545.6	3347.3	ck_0_p_qie		90	90	2590.6	3392.3	**2540.6**	**3342.3**	-120	0
	ck_0_n_qie	2515.6	3437.3	2425.6	3347.3	ck_0_n_qie		90	90	2470.6	3392.3	**2420.6**	**3342.3**	-120	0
	ck_n_in	2395.6	3437.3	2305.6	3347.3	ck_n_in		90	90	2350.6	3392.3	**2300.6**	**3342.3**	-120	0
	ck_p_in	2275.6	3437.3	2185.6	3347.3	ck_p_in		90	90	2230.6	3392.3	**2180.6**	**3342.3**	-120	0
	gnd	2155.6	3437.3	2065.6	3347.3	gnd		90	90	2110.6	3392.3	**2060.6**	**3342.3**	-120	0
	channel_1_reset	2035.6	3437.3	1945.6	3347.3	channel_1_reset		90	90	1990.6	3392.3	**1940.6**	**3342.3**	-120	0
	channel_0_reset	1915.6	3437.3	1825.6	3347.3	channel_0_reset		90	90	1870.6	3392.3	**1820.6**	**3342.3**	-120	0
	test_pulse_trig_1	1795.6	3437.3	1705.6	3347.3	test_pulse_trig_1		90	90	1750.6	3392.3	**1700.6**	**3342.3**	-120	0
	test_pulse_trig_0	1675.6	3437.3	1585.6	3347.3	test_pulse_trig_0		90	90	1630.6	3392.3	**1580.6**	**3342.3**	-120	0
	enable_cal_mode	1555.6	3437.3	1465.6	3347.3	enable_cal_mode		90	90	1510.6	3392.3	**1460.6**	**3342.3**	-120	0
	qie_frorce_range<0>	1435.6	3437.3	1345.6	3347.3	qie_frorce_range<0>		90	90	1390.6	3392.3	**1340.6**	**3342.3**	-120	0
	qie_frorce_range<1>	1315.6	3437.3	1225.6	3347.3	qie_frorce_range<1>		90	90	1270.6	3392.3	**1220.6**	**3342.3**	-120	0
	enable_qie_force_rar	1195.6	3437.3	1105.6	3347.3	enable_qie_force_range		90	90	1150.6	3392.3	**1100.6**	**3342.3**	-120	0
	ped_dac_out<0>	1075.6	3437.3	985.6	3347.3	ped_dac_out<0>		90	90	1030.6	3392.3	**980.6**	**3342.3**	-120	0
	ped_dac_out<1>	955.6	3437.3	865.6	3347.3	ped_dac_out<1>		90	90	910.6	3392.3	**860.6**	**3342.3**	-120	0
	ped_dac_out<2>	835.6	3437.3	745.6	3347.3	ped_dac_out<2>		90	90	790.6	3392.3	**740.6**	**3342.3**	-120	0
	ped_dac_out<3>	715.6	3437.3	625.6	3347.3	ped_dac_out<3>		90	90	670.6	3392.3	**620.6**	**3342.3**	-120	0
	ped_dac_out<4>	595.6	3437.3	505.6	3347.3	ped_dac_out<4>		90	90	550.6	3392.3	**500.6**	**3342.3**	-120	0
	ped_dac_out<5>	475.6	3437.3	385.6	3347.3	ped_dac_out<5>		90	90	430.6	3392.3	**380.6**	**3342.3**	-120	0
	ped_dac_out<6>	355.6	3437.3	265.6	3347.3	ped_dac_out<6>		90	90	310.6	3392.3	**260.6**	**3342.3**	-120	0
	ped_dac_out<7>	235.6	3437.3	145.6	3347.3	ped_dac_out<7>		90	90	190.6	3392.3	**140.6**	**3342.3**	-120	0
	vdd	95	3195.9	5	3105.9	vdd		90	90	50	3150.9	**0**	**3100.9**		
	vdda	95	3075.9	5	2985.9	vdda		90	90	50	3030.9	**0**	**2980.9**	0	-120
	gnda	95	2955.9	5	2865.9	gnda		90	90	50	2910.9	**0**	**2860.9**	0	-120
	vssd	95	2835.9	5	2745.9	vssd		90	90	50	2790.9	**0**	**2740.9**	0	-120
	vddd	95	2715.9	5	2625.9	vddd		90	90	50	2670.9	**0**	**2620.9**	0	-120
	vssd	95	2595.9	5	2505.9	vssd		90	90	50	2550.9	**0**	**2500.9**	0	-120
	gndd	95	2475.9	5	2385.9	gndd		90	90	50	2430.9	**0**	**2380.9**	0	-120
	rst_dll	95	2355.9	5	2265.9	rst_dll		90	90	50	2310.9	**0**	**2260.9**	0	-120
	clr_n_pad	95	2235.9	5	2145.9	clr_n_pad		90	90	50	2190.9	**0**	**2140.9**	0	-120
	beam_crossing_zero	95	2115.9	5	2025.9	beam_crossing_zero		90	90	50	2070.9	**0**	**2020.9**	0	-120
	CCAHWADD<0>	95	1995.9	5	1905.9	CCAHWADD<0>		90	90	50	1950.9	**0**	**1900.9**	0	-120
	CCAHWADD<1>	95	1875.9	5	1785.9	CCAHWADD<1>		90	90	50	1830.9	**0**	**1780.9**	0	-120
	CCAHWADD<2>	95	1755.9	5	1665.9	CCAHWADD<2>		90	90	50	1710.9	**0**	**1660.9**	0	-120
	CCAHWADD<3>	95	1635.9	5	1545.9	CCAHWADD<3>		90	90	50	1590.9	**0**	**1540.9**	0	-120
	CCAHWADD<4>	95	1515.9	5	1425.9	CCAHWADD<4>		90	90	50	1470.9	**0**	**1420.9**	0	-120
	CCAHWADD<5>	95	1395.9	5	1305.9	CCAHWADD<5>		90	90	50	1350.9	**0**	**1300.9**	0	-120
	ck_slow_chip	95	1169.4	5	1079.4	ck_slow_chip		90	90	50	1124.4	**0**	**1074.4**	0	-226.5
	vddd	95	1049.4	5	959.4	vddd		90	90	50	1004.4	**0**	**954.4**	0	-120
	vssd	95	929.4	5	839.4	vssd		90	90	50	884.4	**0**	**834.4**	0	-120
	gndd	95	809.4	5	719.4	gndd		90	90	50	764.4	**0**	**714.4**	0	-120
	data_slow_chip	95	691.8	5	601.8	data_slow_chip		90	90	50	646.8	**0**	**596.8**	0	-117.6
	gnd	95	569.3	5	479.3	gnd		90	90	50	524.3	**0**	**474.3**	0	-122.5
	vssd	95	439.5	5	349.5	vssd		90	90	50	394.5	**0**	**344.5**	0	-129.8
	vdd	95	309.7	5	219.7	vdd		90	90	50	264.7	**0**	**214.7**	0	-129.8

vdd25v	241	95	151	5	vdd25v	90	90	196	50	**146**	**0**			
data_available	361	95	271	5	data_available	90	90	316	50	**266**	**0**	120	0
cntr_available	481	95	391	5	cntr_available	90	90	436	50	**386**	**0**	120	0
send_fill_frame	601	95	511	5	send_fill_frame	90	90	556	50	**506**	**0**	120	0
tx_data<17>	721	95	631	5	tx_data<17>	90	90	676	50	**626**	**0**	120	0
tx_data	841	95	751	5	tx_data	90	90	796	50	**746**	**0**	120	0
tx_data	961	95	871	5	tx_data	90	90	916	50	**866**	**0**	120	0
tx_data	1081	95	991	5	tx_data	90	90	1036	50	**986**	**0**	120	0
tx_data	1201	95	1111	5	tx_data	90	90	1156	50	**1106**	**0**	120	0
tx_data	1321	95	1231	5	tx_data	90	90	1276	50	**1226**	**0**	120	0
tx_data	1441	95	1351	5	tx_data	90	90	1396	50	**1346**	**0**	120	0
tx_data	1561	95	1471	5	tx_data	90	90	1516	50	**1466**	**0**	120	0
tx_data	1681	95	1591	5	tx_data	90	90	1636	50	**1586**	**0**	120	0
tx_data	1801	95	1711	5	tx_data	90	90	1756	50	**1706**	**0**	120	0
tx_data	1921	95	1831	5	tx_data	90	90	1876	50	**1826**	**0**	120	0
tx_data	2041	95	1951	5	tx_data	90	90	1996	50	**1946**	**0**	120	0
tx_data	2161	95	2071	5	tx_data	90	90	2116	50	**2066**	**0**	120	0
tx_data	2281	95	2191	5	tx_data	90	90	2236	50	**2186**	**0**	120	0
tx_data	2401	95	2311	5	tx_data	90	90	2356	50	**2306**	**0**	120	0
tx_data	2521	95	2431	5	tx_data	90	90	2476	50	**2426**	**0**	120	0
tx_data	2641	95	2551	5	tx_data	90	90	2596	50	**2546**	**0**	120	0
tx_data<0>	2761	95	2671	5	tx_data<0>	90	90	2716	50	**2666**	**0**	120	0
gnd	2881	95	2791	5	gnd	90	90	2836	50	**2786**	**0**	120	0
vssd	3001	95	2911	5	vssd	90	90	2956	50	**2906**	**0**	120	0
vdd	3121	95	3031	5	vdd	90	90	3076	50	**3026**	**0**	120	0
exp_0_n<0>	3241	95	3151	5	exp_0_n<0>	90	90	3196	50	**3146**	**0**	120	0
exp_0_p<0>	3361	95	3271	5	exp_0_p<0>	90	90	3316	50	**3266**	**0**	120	0
exp_0_n<1>	3481	95	3391	5	exp_0_n<1>	90	90	3436	50	**3386**	**0**	120	0
exp_0_p<1>	3601	95	3511	5	exp_0_p<1>	90	90	3556	50	**3506**	**0**	120	0
man_0_n<0>	3721	95	3631	5	man_0_n<0>	90	90	3676	50	**3626**	**0**	120	0
man_0_p<0>	3841	95	3751	5	man_0_p<0>	90	90	3796	50	**3746**	**0**	120	0
man_0_n<1>	4076.8	386.2	3986.8	296.2	man_0_n<1>	90	90	4031.8	341.2	**3981.8**	**291.2**		
man_0_n<1>	4076.8	506.2	3986.8	416.2	man_0_n<1>	90	90	4031.8	461.2	**3981.8**	**411.2**	0	120
	4076.8	626.2	3986.8	536.2		90	90	4031.8	581.2	**3981.8**	**531.2**	0	120
	4076.8	746.2	3986.8	656.2		90	90	4031.8	701.2	**3981.8**	**651.2**	0	120
	4076.8	866.2	3986.8	776.2		90	90	4031.8	821.2	**3981.8**	**771.2**	0	120
	4076.8	986.2	3986.8	896.2		90	90	4031.8	941.2	**3981.8**	**891.2**	0	120
	4076.8	1106.2	3986.8	1016.2		90	90	4031.8	1061.2	**3981.8**	**1011.2**	0	120
	4076.8	1226.2	3986.8	1136.2		90	90	4031.8	1181.2	**3981.8**	**1131.2**	0	120
	4076.8	1346.2	3986.8	1256.2		90	90	4031.8	1301.2	**3981.8**	**1251.2**	0	120
	4076.8	1466.2	3986.8	1376.2		90	90	4031.8	1421.2	**3981.8**	**1371.2**	0	120
	4076.8	1586.2	3986.8	1496.2		90	90	4031.8	1541.2	**3981.8**	**1491.2**	0	120
	4076.8	1706.2	3986.8	1616.2		90	90	4031.8	1661.2	**3981.8**	**1611.2**	0	120
	4076.8	1826.2	3986.8	1736.2		90	90	4031.8	1781.2	**3981.8**	**1731.2**	0	120
	4076.8	1946.2	3986.8	1856.2		90	90	4031.8	1901.2	**3981.8**	**1851.2**	0	120
	4076.8	2066.2	3986.8	1976.2		90	90	4031.8	2021.2	**3981.8**	**1971.2**	0	120
	4076.8	2186.2	3986.8	2096.2		90	90	4031.8	2141.2	**3981.8**	**2091.2**	0	120
	4076.8	2306.2	3986.8	2216.2		90	90	4031.8	2261.2	**3981.8**	**2211.2**	0	120
	4076.8	2426.2	3986.8	2336.2		90	90	4031.8	2381.2	**3981.8**	**2331.2**	0	120
	4076.8	2546.2	3986.8	2456.2		90	90	4031.8	2501.2	**3981.8**	**2451.2**	0	120
	4076.8	2666.2	3986.8	2576.2		90	90	4031.8	2621.2	**3981.8**	**2571.2**	0	120
	4076.8	2786.2	3986.8	2696.2		90	90	4031.8	2741.2	**3981.8**	**2691.2**	0	120
	4076.8	2906.2	3986.8	2816.2		90	90	4031.8	2861.2	**3981.8**	**2811.2**	0	120
man_1_n<3>	4076.8	3026.2	3986.8	2936.2	man_1_n<3>	90	90	4031.8	2981.2	**3981.8**	**2931.2**	0	120
man_1_p<3>	4076.8	3146.2	3986.8	3056.2	man_1_p<3>	90	90	4031.8	3101.2	**3981.8**	**3051.2**	0	120