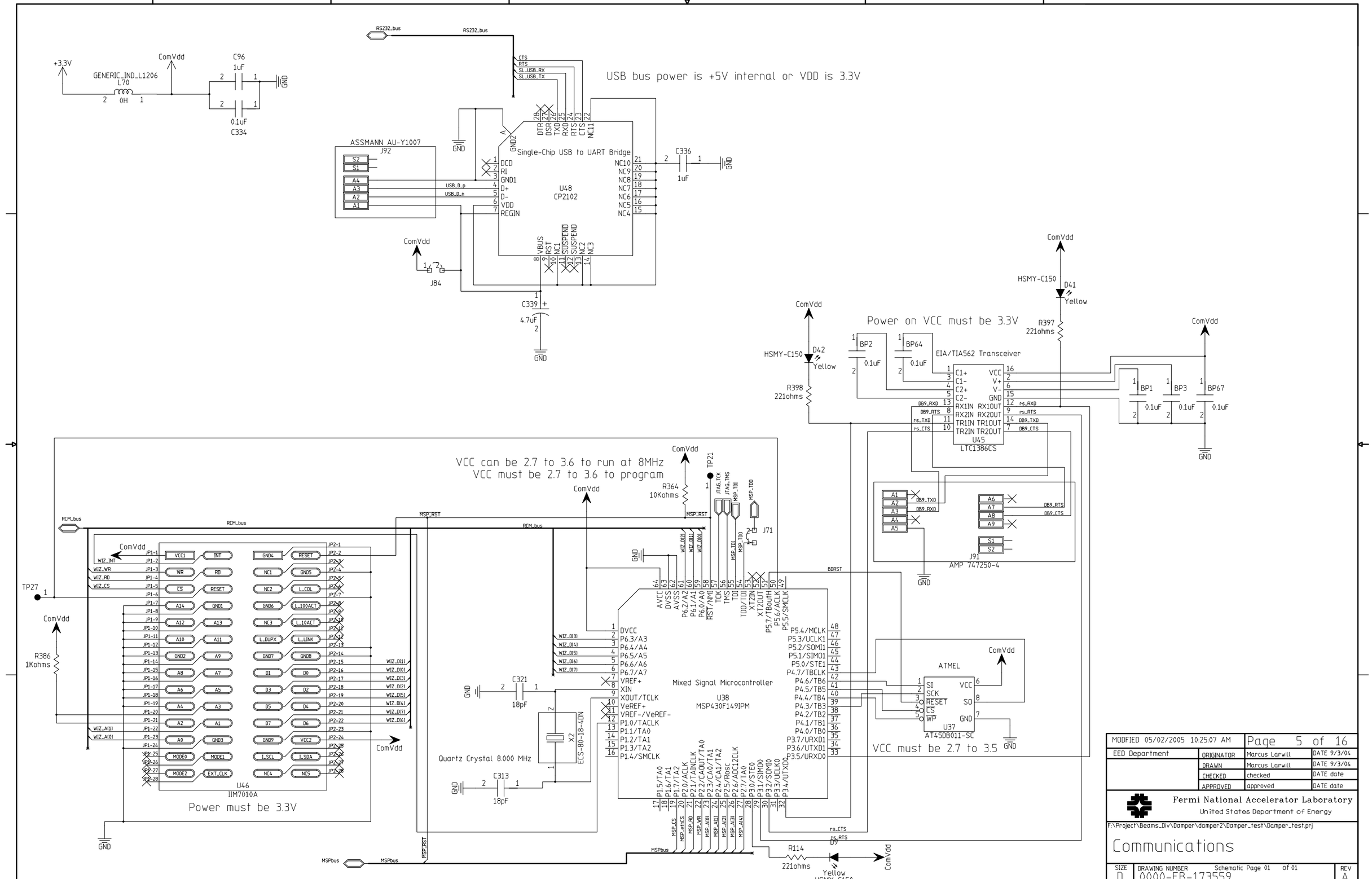
USB bus power is +5V internal or VDD is 3.3V
Single-Chip USB to UART Bridge
U48
CP2102
ASSMANN AU-Y1007
J92
Power on VCC must be 3.3V
EIA/TIA562 Transceiver
U45
LTC1386CS
AMP 747250-4
J91
VCC can be 2.7 to 3.6 to run at 8MHz
VCC must be 2.7 to 3.6 to program
Mixed Signal Microcontroller
U38
MSP430F149IPM
Quartz Crystal 8.000 MHz
ATMEL
AT45DB011-SC
U37
VCC must be 2.7 to 3.5
U46
IIM7010A
Power must be 3.3V
MODIFIED 05/02/2005 10:25:07 AM
Page 5 of 16
EED Department
ORIGINATOR Marcus Larwill DATE 9/3/04
DRAWN Marcus Larwill DATE 9/3/04
CHECKED checked DATE date
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Fermi National Accelerator Laboratory
United States Department of Energy
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Communications
SIZE D
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Schematic Page 01 of 01
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