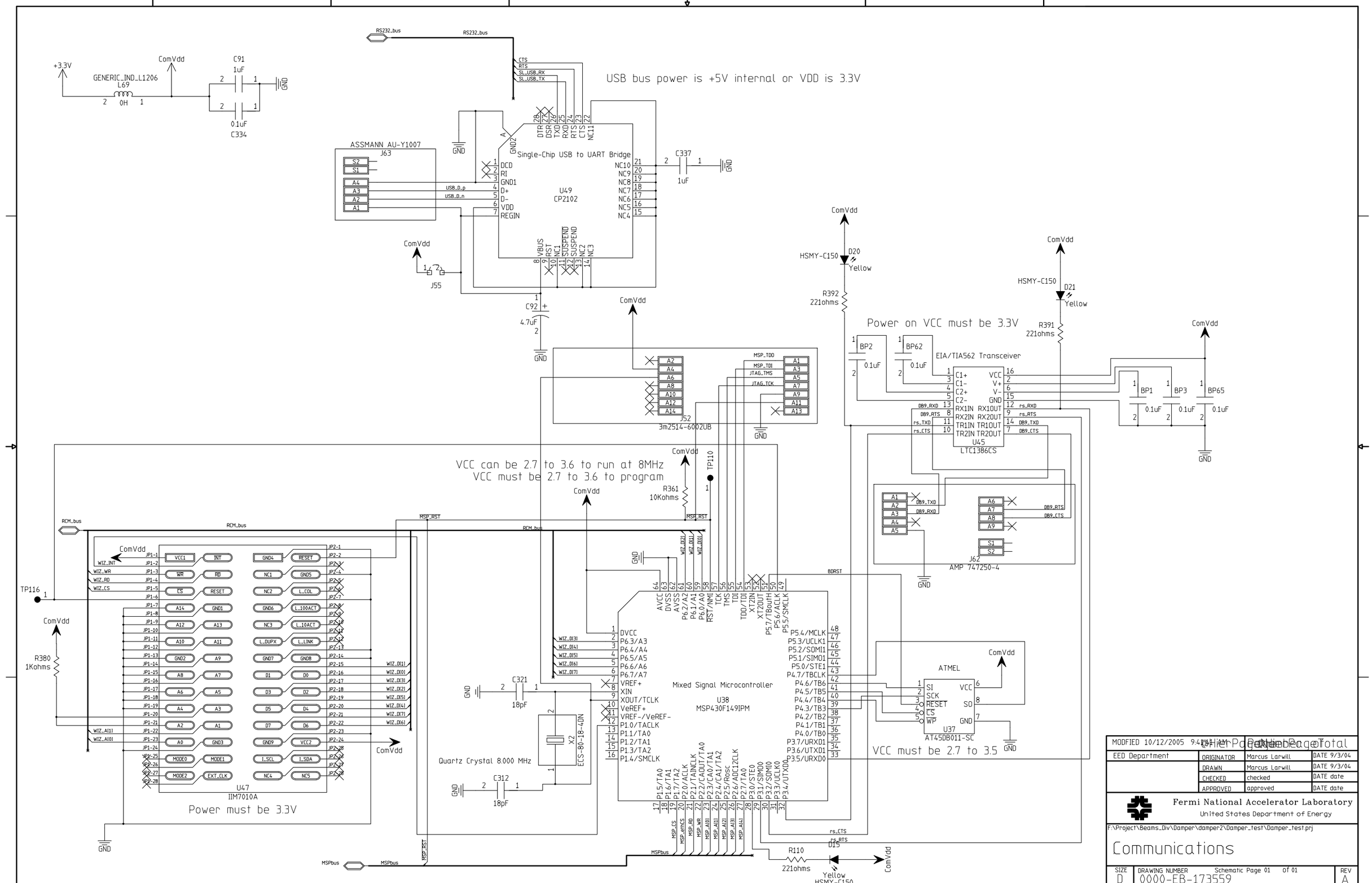
USB bus power is +5V internal or VDD is 3.3V
Single-Chip USB to UART Bridge
U49
CP2102
ASSMANN AU-Y1007
J63
Power on VCC must be 3.3V
EIA/TIA562 Transceiver
U45
LTC1386CS
J52
3m2514-6002UB
J62
AMP 747250-4
VCC can be 2.7 to 3.6 to run at 8MHz
VCC must be 2.7 to 3.6 to program
Mixed Signal Microcontroller
U38
MSP430F149IPM
Quartz Crystal 8.000 MHz
U47
IIM7010A
Power must be 3.3V
ATMEL
AT45DB011-SC
U37
VCC must be 2.7 to 3.5
MODIFIED 10/12/2005
EED Department
ORIGINATOR Marcus Larwill
DRAWN Marcus Larwill
CHECKED checked
APPROVED approved
Fermi National Accelerator Laboratory
United States Department of Energy
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Communications
SIZE D
DRAWING NUMBER 0000-EB-173559
Schematic Page 01 of 01
REV A