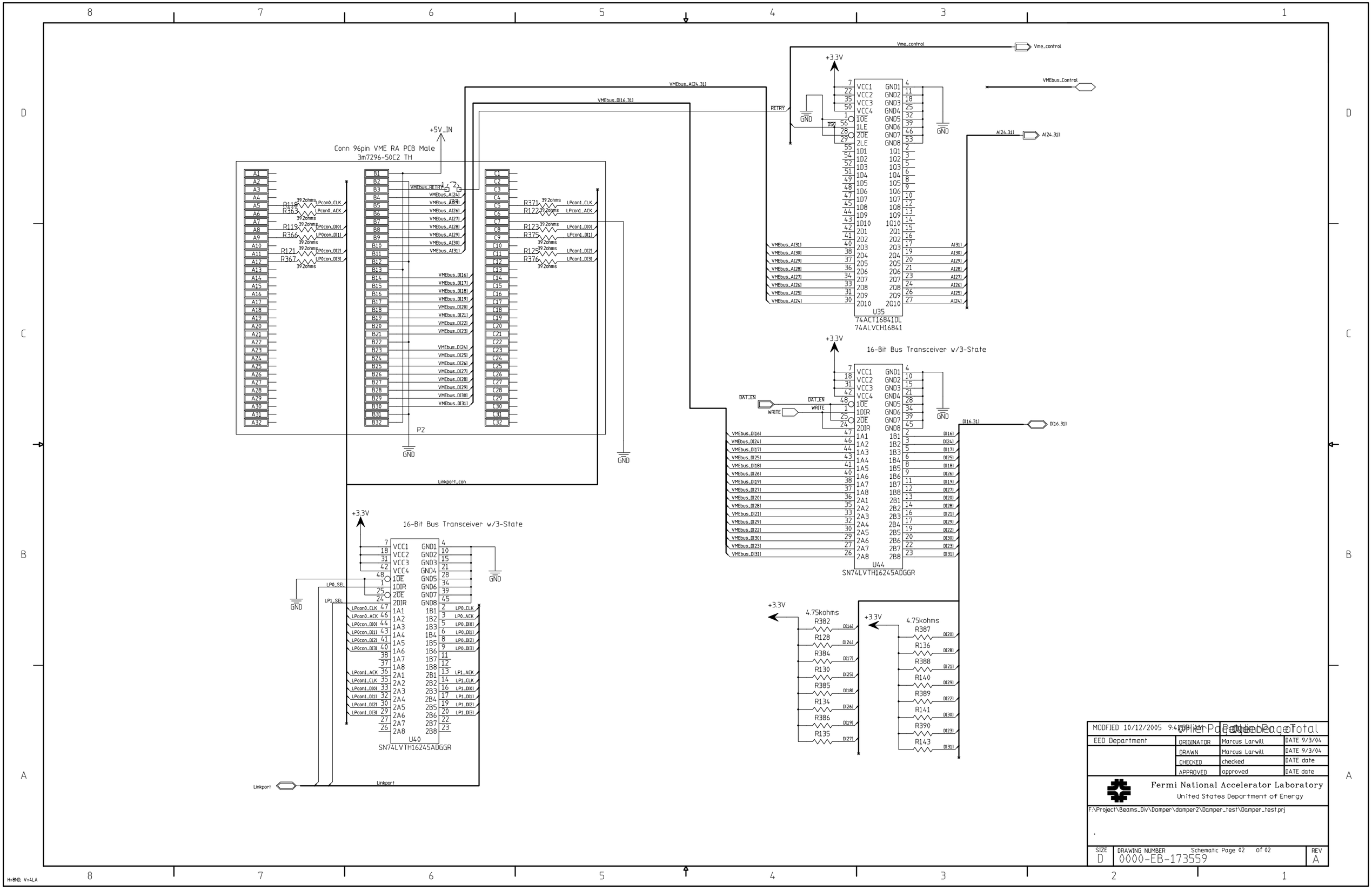

Conn 96pin VME RA PCB Male
3m7296-50C2 TH
+5V_IN
P2
GND
Linkport_con
Vme_control
VMEbus_Control
VMEbus_A[24..31]
VMEbus_D[16..31]
RETRY
+3.3V
U35
74ACT16841DL
74ALVCH16841
16-Bit Bus Transceiver w/3-State
U44
SN74LVTH16245ADGGR
DAT_EN
WRITE
D[16..31]
4.75kohms
R382
R128
R384
R130
R385
R134
R386
R135
R387
R136
R388
R140
R389
R141
R390
R143
U40
SN74LVTH16245ADGGR
LP0_SEL
LP1_SEL
Linkport
MODFIED 10/12/2005 9:41:08 AM
EED Department
ORIGINATOR Marcus Larwill DATE 9/3/04
DRAWN Marcus Larwill DATE 9/3/04
CHECKED checked DATE date
APPROVED approved DATE date
Fermi National Accelerator Laboratory
United States Department of Energy
F:\Project\Beams_Div\Damper\damper2\Damper_test\Damper_test.prj
SIZE D
DRAWING NUMBER 0000-EB-173559
Schematic Page 02 of 02
REV A