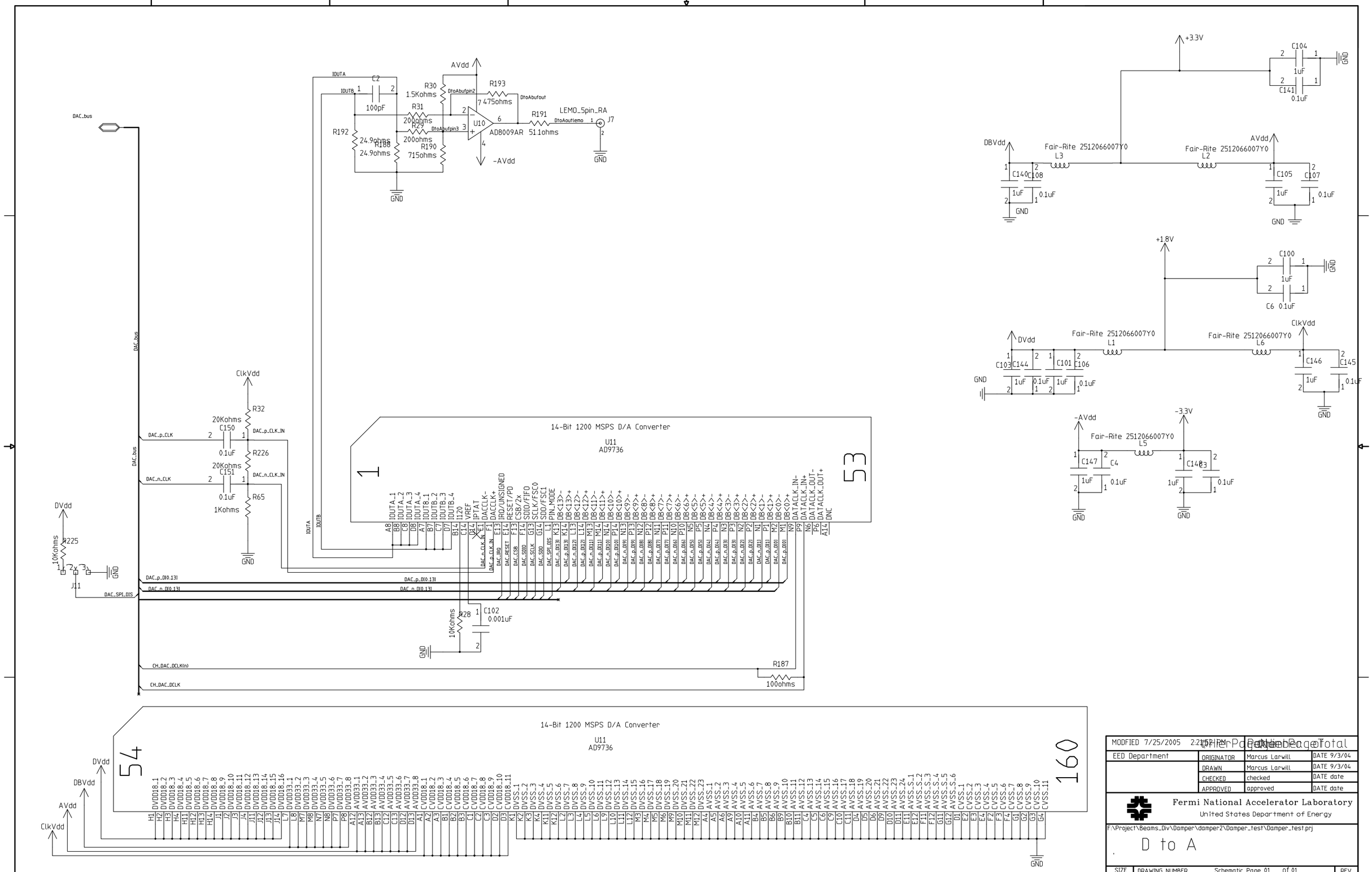
AVdd
IOUTA
IOUTB
C2
100pF
R30
1.5Kohms
R193
475ohms
DtoAbufpin2
DtoAbufout
R31
200ohms
R29
200ohms
DtoAbufpin3
U10
AD8009AR
R191
51.1ohms
LEMO_5pin_RA
DtoAoutlemo
J7
R192
24.9ohms
R188
24.9ohms
R190
715ohms
-AVdd
GND
DAC_bus
ClkVdd
R32
20Kohms
C150
0.1uF
R226
20Kohms
C151
0.1uF
R65
1Kohms
DAC_p_CLK
DAC_p_CLK_IN
DAC_n_CLK
DAC_n_CLK_IN
DVdd
R225
10Kohms
J11
DAC_SPI_DIS
14-Bit 1200 MSPS D/A Converter
U11
AD9736
1
53
DAC_p_D[0.13]
DAC_n_D[0.13]
R28
10Kohms
C102
0.001uF
CH_DAC_DCLK(n)
CH_DAC_DCLK
R187
100ohms
54
160
DBVdd
+3.3V
C104
1uF
C141
0.1uF
Fair-Rite 2512066007Y0
L3
L2
C140
C108
C105
C107
+1.8V
C100
C6
L1
L6
C103
C144
C101
C106
C146
C145
-AVdd
-3.3V
L5
C147
C4
C148
C3
MODIFIED 7/25/2005 2:21:51 PM
EED Department
ORIGINATOR Marcus Larwill DATE 9/3/04
DRAWN Marcus Larwill DATE 9/3/04
CHECKED checked DATE date
APPROVED approved DATE date
Fermi National Accelerator Laboratory
United States Department of Energy
F:\Project\Beams_Div\Damper\damper2\Damper_test\Damper_test.prj
D to A
SIZE D
DRAWING NUMBER 0000-EB-173559
Schematic Page 01 of 01
REV A
H=BND, V=4LA