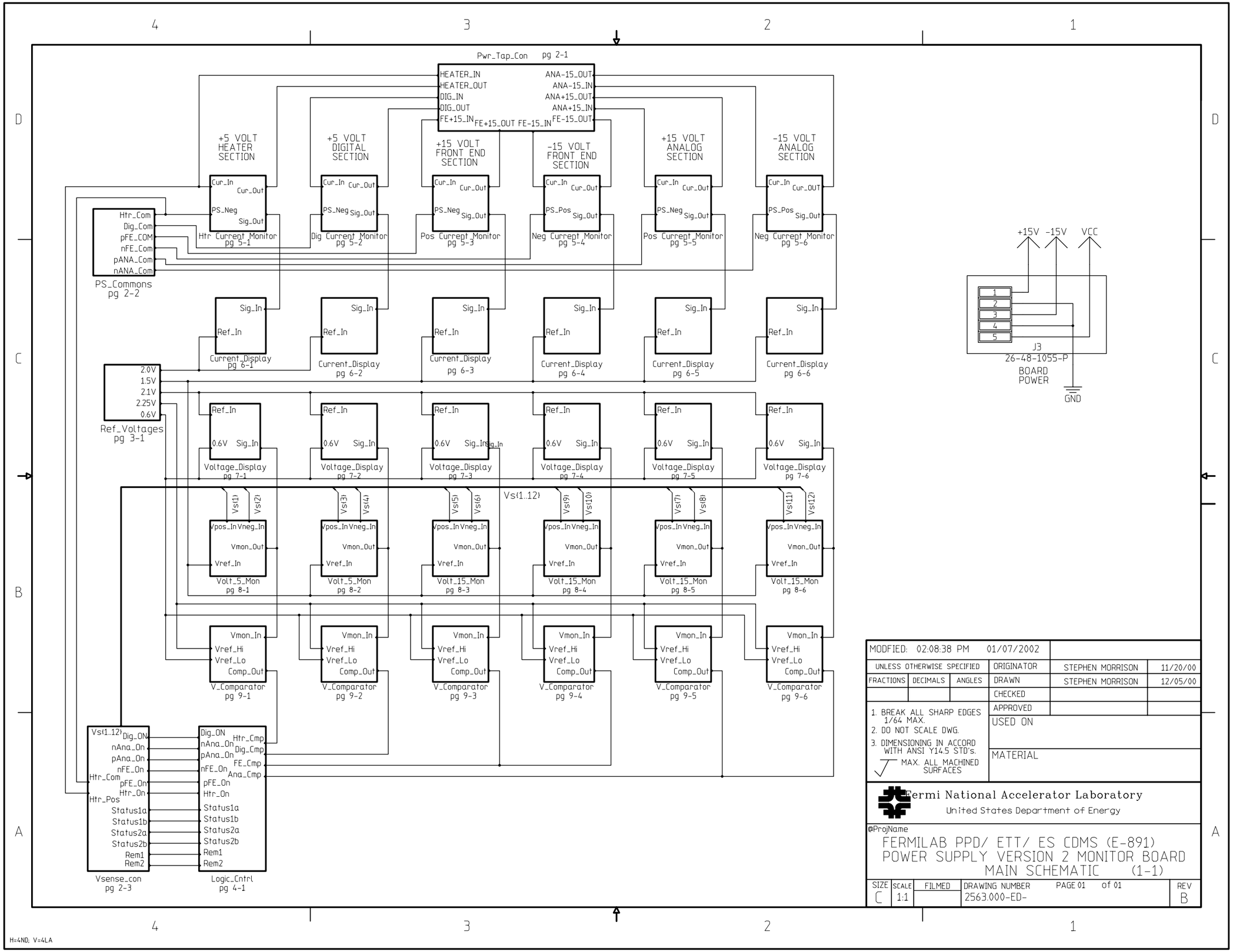
Pwr_Tap_Con pg 2-1
HEATER_IN
HEATER_OUT
DIG_IN
DIG_OUT
FE+15_IN
FE+15_OUT
FE-15_IN
ANA-15_OUT
ANA-15_IN
ANA+15_OUT
ANA+15_IN
FE-15_OUT
+5 VOLT HEATER SECTION
+5 VOLT DIGITAL SECTION
+15 VOLT FRONT END SECTION
-15 VOLT FRONT END SECTION
+15 VOLT ANALOG SECTION
-15 VOLT ANALOG SECTION
Htr Current Monitor pg 5-1
Dig Current Monitor pg 5-2
Pos Current Monitor pg 5-3
Neg Current Monitor pg 5-4
Pos Current Monitor pg 5-5
Neg Current Monitor pg 5-6
PS_Commons pg 2-2
Htr_Com
Dig_Com
pFE_COM
nFE_Com
pANA_Com
nANA_Com
Current_Display pg 6-1
Current_Display pg 6-2
Current_Display pg 6-3
Current_Display pg 6-4
Current_Display pg 6-5
Current_Display pg 6-6
Ref_Voltages pg 3-1
2.0V
1.5V
2.1V
2.25V
0.6V
Voltage_Display pg 7-1
Voltage_Display pg 7-2
Voltage_Display pg 7-3
Voltage_Display pg 7-4
Voltage_Display pg 7-5
Voltage_Display pg 7-6
Vs{1..12}
Volt_5_Mon pg 8-1
Volt_5_Mon pg 8-2
Volt_15_Mon pg 8-3
Volt_15_Mon pg 8-4
Volt_15_Mon pg 8-5
Volt_15_Mon pg 8-6
V_Comparator pg 9-1
V_Comparator pg 9-2
V_Comparator pg 9-3
V_Comparator pg 9-4
V_Comparator pg 9-5
V_Comparator pg 9-6
Vsense_con pg 2-3
Logic_Cntrl pg 4-1
+15V
-15V
VCC
J3
26-48-1055-P
BOARD POWER
GND
MODIFIED: 02:08:38 PM 01/07/2002
UNLESS OTHERWISE SPECIFIED
FRACTIONS DECIMALS ANGLES
ORIGINATOR STEPHEN MORRISON 11/20/00
DRAWN STEPHEN MORRISON 12/05/00
CHECKED
APPROVED
1. BREAK ALL SHARP EDGES 1/64 MAX.
2. DO NOT SCALE DWG.
3. DIMENSIONING IN ACCORD WITH ANSI Y14.5 STD's.
MAX. ALL MACHINED SURFACES
USED ON
MATERIAL
Fermi National Accelerator Laboratory
United States Department of Energy
@ProjName
FERMILAB PPD/ ETT/ ES CDMS (E-891)
POWER SUPPLY VERSION 2 MONITOR BOARD
MAIN SCHEMATIC (1-1)
SIZE C
SCALE 1:1
FILMED
DRAWING NUMBER 2563.000-ED-
PAGE 01 of 01
REV B
H=4ND; V=4LA