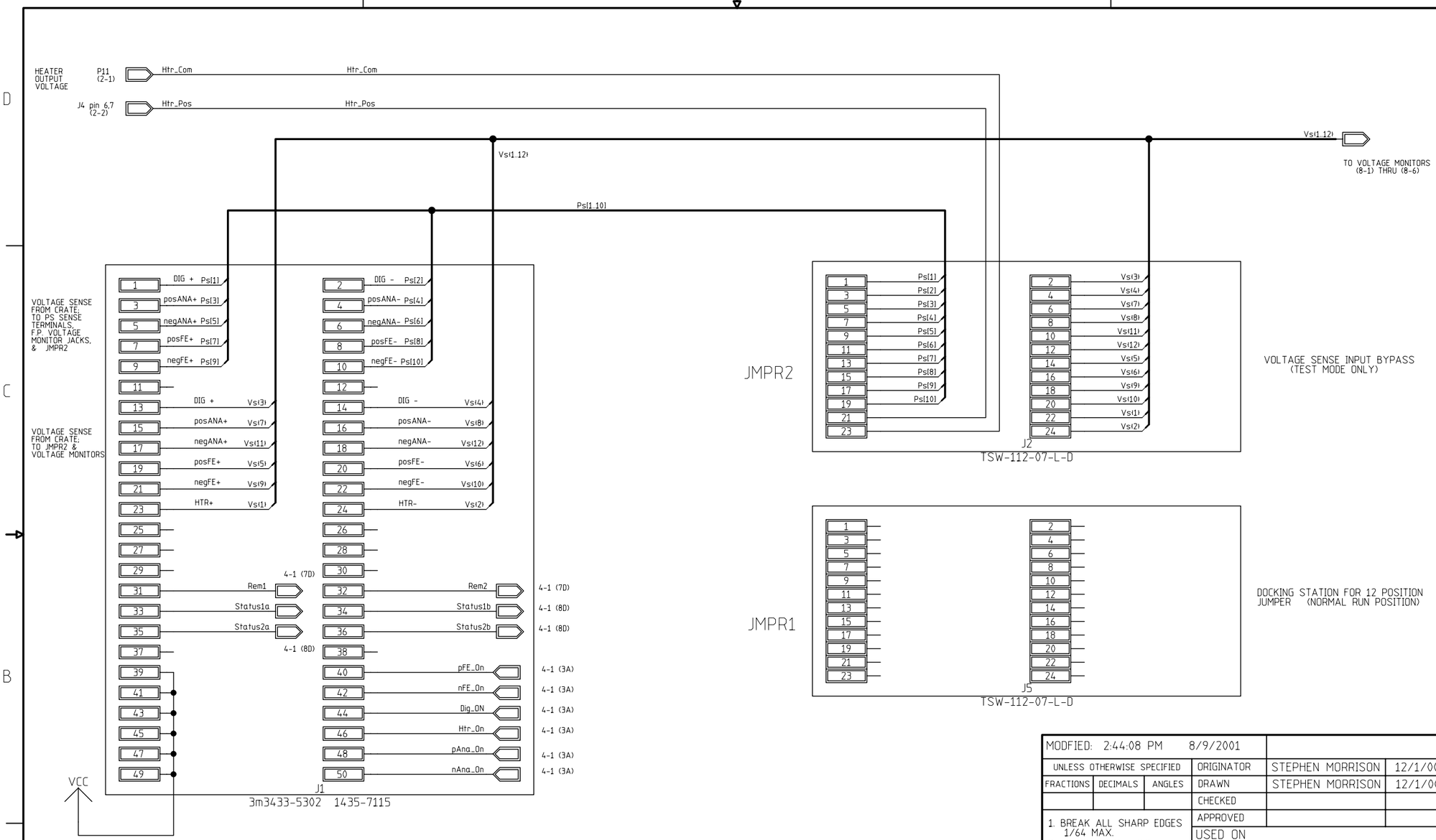

NOTES: 1. IN ORDER TO TEST A CDMS POWER SUPPLY CHASSIS WITHOUT THE SENSE LEADS CONNECTED TO A CRATE, PLACE A SHUNT ACROSS THE TERMINALS OF JMPR2. THIS GIVES THE VOLTAGE MONITOR CIRCUITS AN INPUT FROM THE POWER SUPPLY SENSE TERMINALS. SINCE THE HEATER SUPPLY DOES NOT HAVE SENSE TERMINALS, THE OUTPUT VOLTAGE IS BROUGHT TO JUMPR2.

MODFIED: 2:44:08 PM 8/9/2001					
UNLESS OTHERWISE SPECIFIED			ORIGINATOR	STEPHEN MORRISON	12/1/00
FRACTIONS	DECIMALS	ANGLES	DRAWN	STEPHEN MORRISON	12/1/00
			CHECKED		
1. BREAK ALL SHARP EDGES 1/64 MAX. 2. DO NOT SCALE DWG. 3. DIMENSIONING IN ACCORD WITH ANSI Y14.5 STD's.			APPROVED		
			USED ON		
MAX. ALL MACHINED SURFACES			MATERIAL		

Fermi National Accelerator Laboratory
United States Department of Energy

@ProjName
FERMILAB PPD/ ETT/ ES CDMS (E-891)
POWER SUPPLY VER. 2 MONITOR BOARD
VOLTAGE SENSE I/O CONNECTOR (2-3)

SIZE	SCALE	FILMED	DRAWING NUMBER	PAGE 01 of 01	REV
C	1:1		2563.000-ED-		B

H=4ND; V=4LA