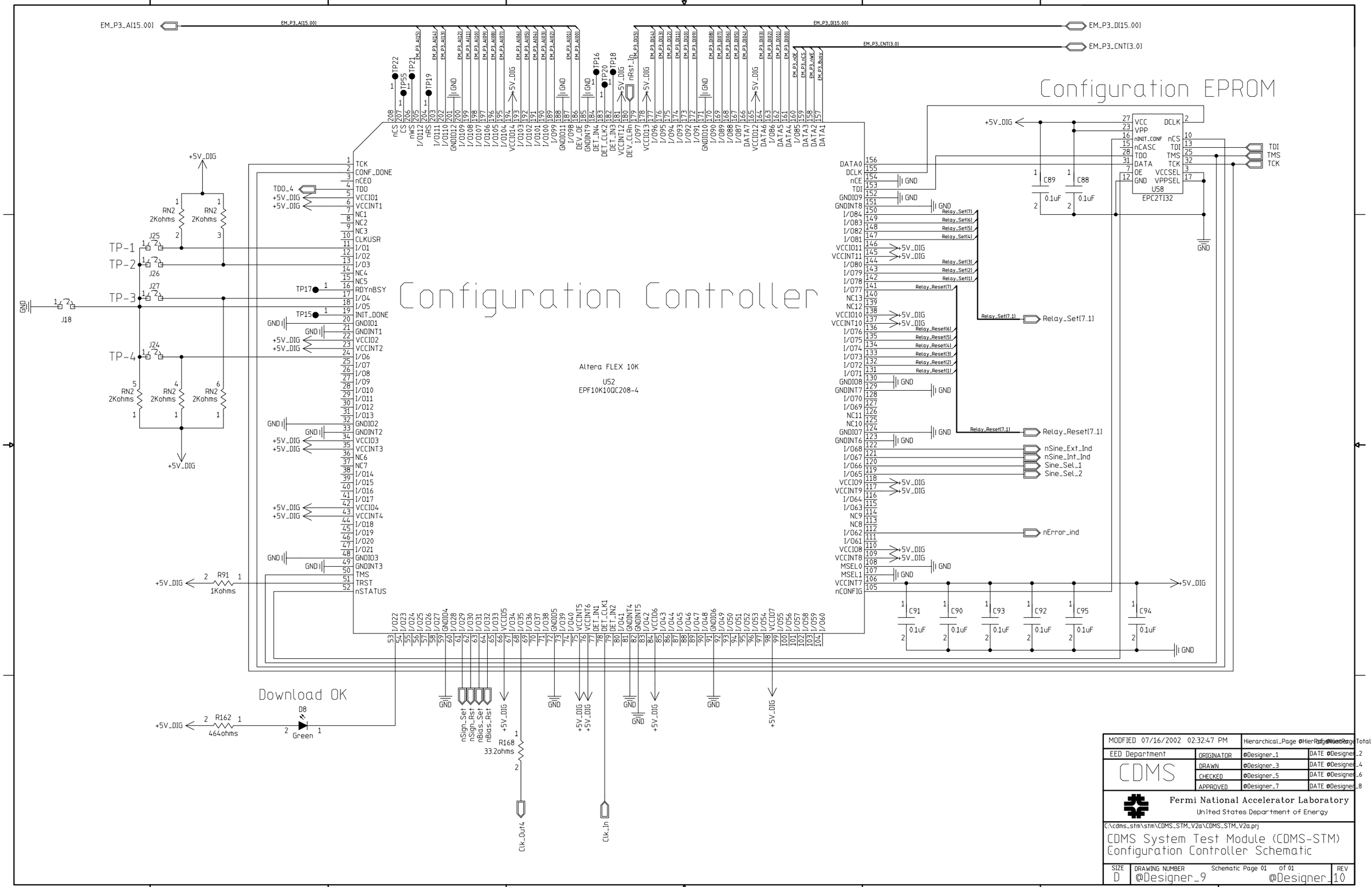

Configuration EPROM
Configuration Controller
Altera FLEX 10K
U52
EPF10K10QC208-4
Download OK
EM_P3_A[15..00]
EM_P3_D[15..00]
EM_P3_CNT[3..0]
TP-1
TP-2
TP-3
TP-4
J18
J25
J26
J27
J24
RN2
2Kohms
TDO_4
TP17
TP15
TCK
CONF_DONE
nCEO
TDO
VCCIO1
VCCINT1
NC1
NC2
NC3
CLKUSR
I/O1
I/O2
I/O3
NC4
NC5
RDYnBSY
I/O4
I/O5
INIT_DONE
GNDIO1
GNDINT1
VCCIO2
VCCINT2
I/O6
I/O7
I/O8
I/O9
I/O10
I/O11
I/O12
I/O13
GNDIO2
GNDINT2
VCCIO3
VCCINT3
NC6
NC7
I/O14
I/O15
I/O16
I/O17
VCCIO4
VCCINT4
I/O18
I/O19
I/O20
I/O21
GNDIO3
GNDINT3
TMS
TRST
nSTATUS
DATA0
DCLK
nCE
TDI
GNDIO9
GNDINT8
I/O84
I/O83
I/O82
I/O81
VCCIO11
VCCINT11
I/O80
I/O79
I/O78
I/O77
NC13
NC12
VCCIO10
VCCINT10
I/O76
I/O75
I/O74
I/O73
I/O72
I/O71
GNDIO8
GNDINT7
I/O70
I/O69
NC11
NC10
GNDIO7
GNDINT6
I/O68
I/O67
I/O66
I/O65
VCCIO9
VCCINT9
I/O64
I/O63
NC9
NC8
I/O62
I/O61
VCCIO8
VCCINT8
MSEL0
MSEL1
VCCINT7
nCONFIG
Relay_Set[7..1]
Relay_Reset[7..1]
nSine_Ext_Ind
nSine_Int_Ind
Sine_Sel_1
Sine_Sel_2
nError_ind
VCC
VPP
nINIT_CONF
nCASC
TDO
DATA
OE
GND
DCLK
nCS
TDI
TMS
TCK
VCCSEL
VPPSEL
U58
EPC2TI32
C89
C88
0.1uF
C91
C90
C93
C92
C95
C94
R91
1Kohms
R162
464ohms
D8
Green
R168
33.2ohms
nSign_Set
nSign_Rst
nBias_Set
nBias_Rst
Clk_Out4
Clk_In
+5V_DIG
GND
MODIFIED 07/16/2002 02:32:47 PM
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@HierPageTotal
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CDMS
ORIGINATOR @Designer_1
DATE @Designer_2
DRAWN @Designer_3
DATE @Designer_4
CHECKED @Designer_5
DATE @Designer_6
APPROVED @Designer_7
DATE @Designer_8
Fermi National Accelerator Laboratory
United States Department of Energy
C:\cdms_stm\stm\CDMS_STM_V2a\CDMS_STM_V2a.prj
CDMS System Test Module (CDMS-STM)
Configuration Controller Schematic
SIZE D
DRAWING NUMBER @Designer_9
Schematic Page 01 of 01
REV @Designer_10
H=8ND; V=4LA