STM VER 3 MODULE ADDRESSING

This document can be found at the following location
\\Ppdserver\EED.PPD\Project\CDMS\module\9u\STM

UNUSED			Subrack SLOT					SECTION				SUBSECTION			
15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0

SECTIONS

F	SPARE
E	EVENT
D	DELAY
C	SPARE
B	RISE TIME
A	AMPLITUDE
9	SPARE
8	TESTING
7	SPARE
6	SPARE
5	SPARE
4	SPARE
3	SPARE
2	SPARE
1	MEMORY
0	MODULE INFO

SUBSECTIONS

	0 COMMAND	1 MEMORY	2	3	4
F	GPIB FIRE				
E		EVENT #			
D	GPIB DWNLD	RD/nWR Data			
C					
B		EVENT PNTR			
A		Memory Address			
9					
8					
7					
6					
5	START/nSTOP				
4					
3					
2					
1	RESET				
0	ID				

	8 TESTING	9	A AMPLITUDE	B RISETIME	C	D DELAY	E EVENT	F
F			ALL PULSER AMPLITUDE	ALL PULSERS RISE TIME		ALL PULSERS DELAY TIME	TG FIRE	
E	EXT SIG ENA						STRT EXT CYCLE	
D			PULSER D AMPLITUDE	PULSER D RISE TIME		PULSER D DELAY TIME	TG DWNLD	
C			PULSER C AMPLITUDE	PULSER C RISE TIME		PULSER C DELAY TIME	STRT EVENT CYCLE	
B			PULSER B AMPLITUDE	PULSER B RISE TIME		PULSER B DELAY TIME	STOP SEQUENCER	
A			PULSER A AMPLITUDE	PULSER A RISE TIME		PULSER A DELAY TIME		
9								
8								
7								
6								
5							STRT MEM CYCLE	
4								
3								
2								
1	INT SIG ENA		Qi PULSER AMPLITUDE				TG ^t HI	
0	Test Point Configuration		Qo PULSER AMPLITUDE				TG ^t LO	

Notes:

1. For addressing purposes, modules are divided into 16 sections, each having a 4 bit address. Each section is divided into 16 subsections. For example, in order to address the Phonon B Rise Time of an STM Module plugged into Subrack-SLOT 7, you would use address xx00 0111 1011 1011; or hexadecimal address 07BB

2.

Available for possible future addition

7	
6	
5	
4	MULTI EVENTS CONTINUOUS
3	MULTI EVENTS ONCE
2	SINGLE EVENT CONTINUOUS
1	SINGLE EVENT ONCE
0	OFF

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