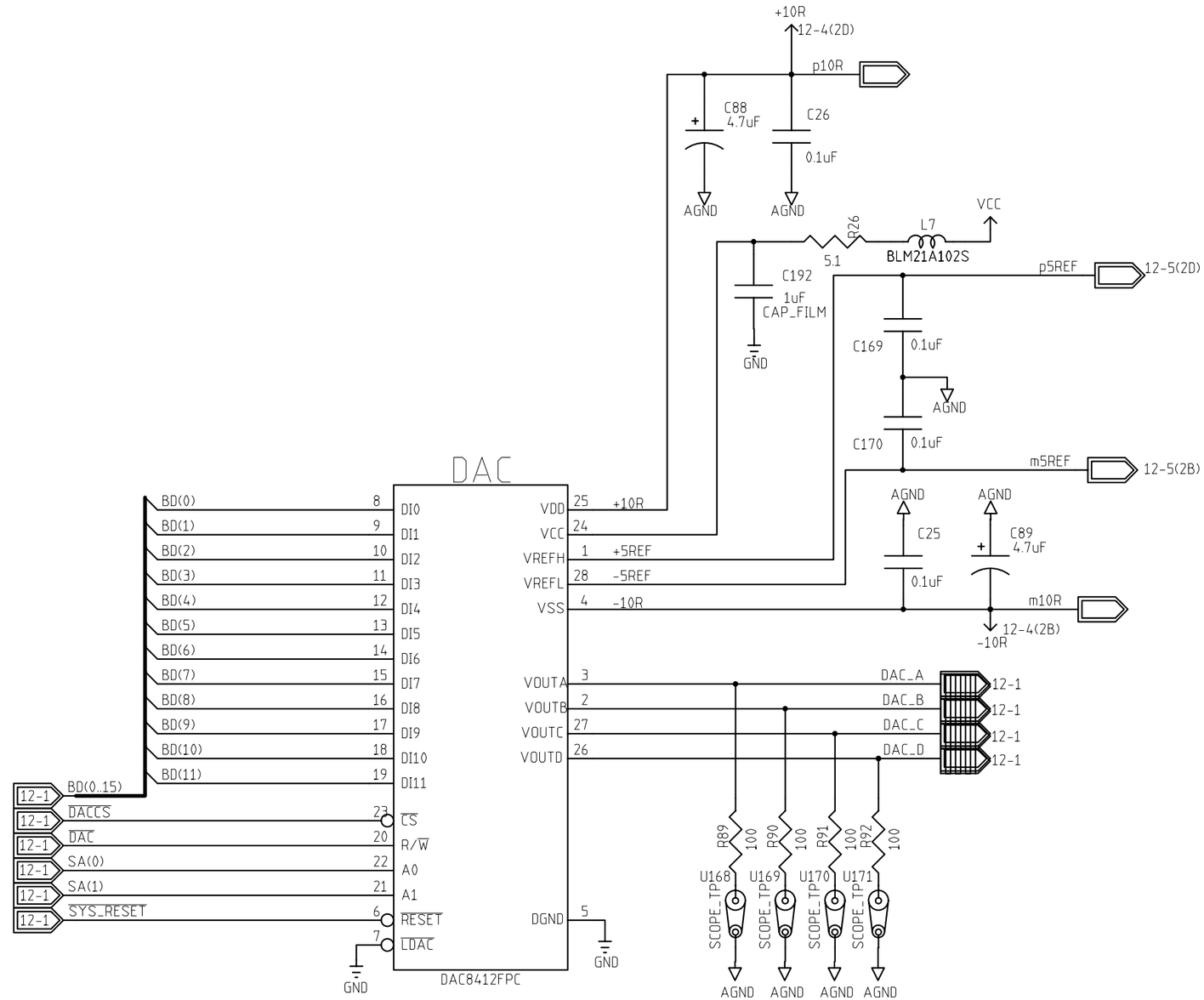
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
	UNDER DEVELOPMENT	1-19-99	



1: ALL RESISTORS ARE 1% UNLESS OTHERWISE STATED
2: ALL LOGIC LEVELS ARE HIGH TRUE

	NAME	DATE
ORIGINATOR	MERLE HALDEMAN	11/17/98
DRAWN	BRUCE MERKEL	11/17/98
CHECKED	TOM CRENNA	1/8/99
APPROVED		

FERMILAB PPD/ ETT/ ES CDMS(E-891)
ZIP MODULE
DAC'S_1_TO_7

SIZE	FSCM NO.	DWG. NO.	REV
B			1
SCALE		SHEET 12-6	

U32