ZIP MODULE ADDRESSING

UNUSED			Subrack SLOT					SECTION				SUBSECTION			
15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0

Available for possible future addition

	SECTIONS	SUBSECTIONS 0	1	2	3	4	5	6	7	E	F
F	COMMAND	RESET									Test Pulse
E	CSR	SPARE									FET Heater
D	SPARE	SPARE	DAC 3 SENSOR BIAS D	DAC 3 SQUID BIAS D	DAC 3 SQUID LOCK D	DAC 3 SQUID GAIN D	DAC 3 SQUID D DRIVER OFFSET				
C	SPARE	SPARE	DAC 2 SENSOR BIAS C	DAC 2 SQUID BIAS C	DAC 2 SQUID LOCK C	DAC 2 SQUID GAIN C	DAC 2 SQUID C DRIVER OFFSET				
B	SPARE	SPARE	DAC 1 SENSOR BIAS B	DAC 1 SQUID BIAS B	DAC 1 SQUID LOCK B	DAC 1 SQUID GAIN B	DAC 1 SQUID B DRIVER OFFSET				
A	SPARE	SPARE	DAC 0 SENSOR BIAS A	DAC 0 SQUID BIAS A	DAC 0 SQUID LOCK A	DAC 0 SQUID GAIN A	DAC 0 SQUID A DRIVER OFFSET				
9	SPARE	SPARE									
8	SPARE	SPARE									
7	Q BIAS DAC	SPARE									
6	Q DRIVER DAC	+15A									
5	SQUID DRIVER DAC	-15A								CSR5	
4	SQUID GAIN DAC	+15FE								CSR4	
3	SQUID LOCK DAC	-15FE							DAC 3 ZAP VOLTAGE	CSR3	
2	SQUID BIAS DAC	+5						DAC 2 TEST VOLTAGE	DAC 2 LED BIAS	CSR2	
1	SENSOR BIAS DAC	TEMP						DAC 1 Qi DRIVER OFFSET	DAC 1 Qi BIAS	CSR1	
0	MODULE INFO.	I D						DAC 0 Qo DRIVER OFFSET	DAC 0 Qo BIAS	CSR0	

Notes:

1. For addressing purposes, modules are divided into 16 sections, each having a 4 bit address. Each section is divided into 16 subsections. For example, in order to address the FET Heater of a ZIP Module plugged into Subrack-SLOT 7, you would use address 0011111111110; or hexadecimal address 07FE
2. Command Column (F) items only require a write command to the address to execute.

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