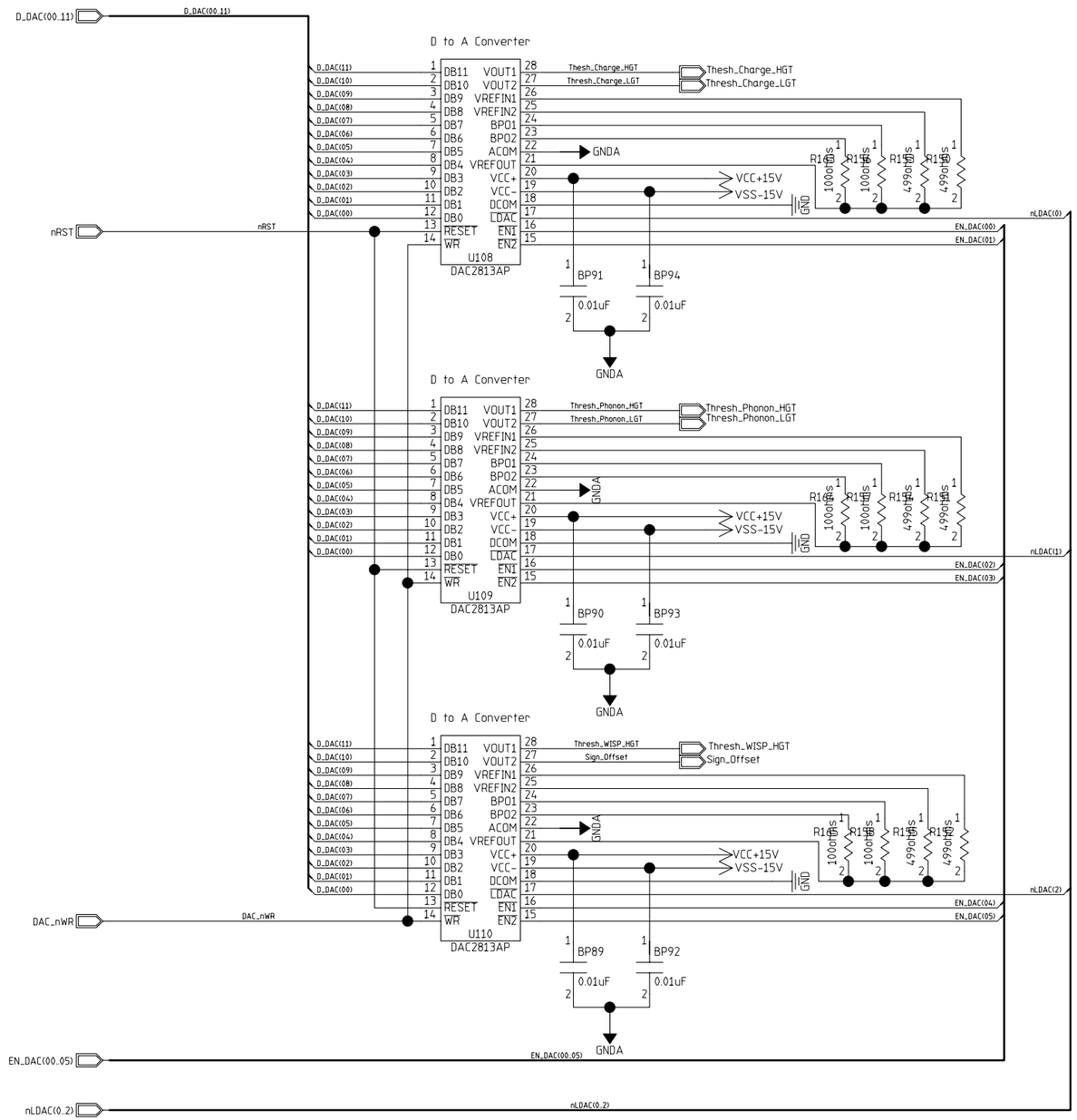

Charge Threshold DAC

Phonon Threshold DAC

WISP Threshold & Offset DAC

MODIFIED 03:33:53 PM 10/07/2002				
UNLESS OTHERWISE SPECIFIED			ORIGINATOR	Mark Kozlovsky
FRACTIONS	DECIMALS	ANGLES	DRAWN	Mark Kozlovsky
			CHECKED	
			APPROVED	

1. BREAK ALL SHARP EDGES 1/64 MAX.
2. DO NOT SCALE DWG.
3. DIMENSIONING IN ACCORD WITH ANSI Y14.5 STD's.

MAX. ALL MACHINED SURFACES

USED ON CDMS

MATERIAL

Fermi National Accelerator Laboratory
United States Department of Energy

C:\cdms_rtf\rtf\CDMS_RTF_V3\CDMS_RTF_V3.prj

ZIP RTF MODULE (RTF)
Threshold and Offset DACs

SIZE	SCALE	FILMED	DRAWING NUMBER	PAGE 01 of 01	REV
D	1:1				A

H=8ND; V=4LA