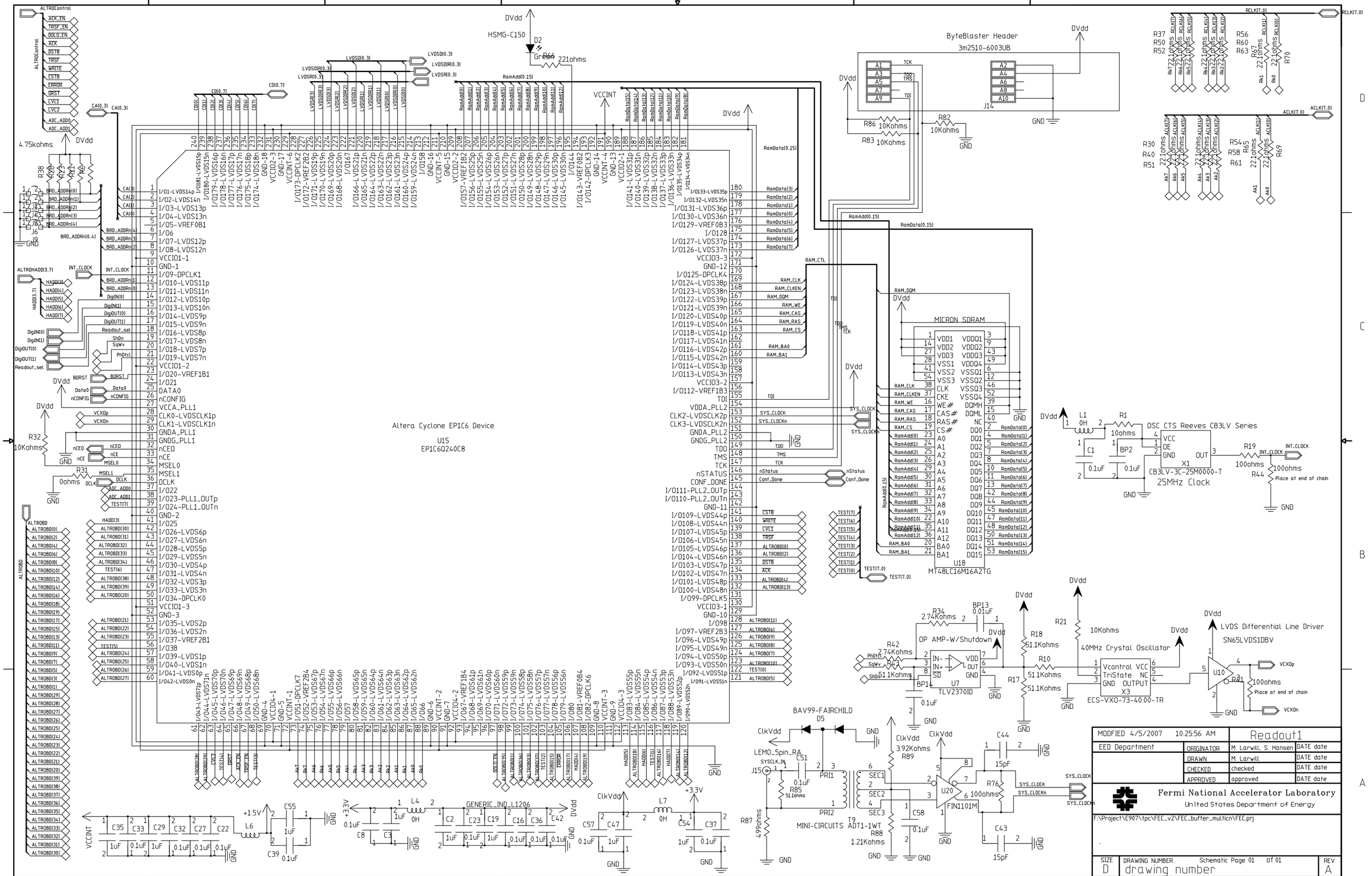
Altera Cyclone EP1C6 Device
U15
EP1C6Q240C8
MICRON SDRAM
MT48LC16M16A2TG
U18
ByteBlaster Header
3m2510-6003UB
J14
HSMG-C150
D2
R66 221ohms
OSC CTS Reeves CB3LV Series
CB3LV-3C-25M0000-T
25MHz Clock
X1
40MHz Crystal Oscillator
ECS-VXO-73-40.00-TR
X3
LVDS Differential Line Driver
SN65LVDS1DBV
U10
OP AMP-W/Shutdown
TLV2370ID
U7
BAV99-FAIRCHILD
D5
LEMO_5pin_RA
J15
MINI-CIRCUITS ADT1-1WT
T9
FIN1101M
U20
GENERIC_IND_L1206
J6
MODIFIED 4/5/2007 10:25:56 AM
Readout1
EED Department
ORIGINATOR M. Larwill, S. Hansen DATE date
DRAWN M. Larwill DATE date
CHECKED checked DATE date
APPROVED approved DATE date
Fermi National Accelerator Laboratory
United States Department of Energy
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