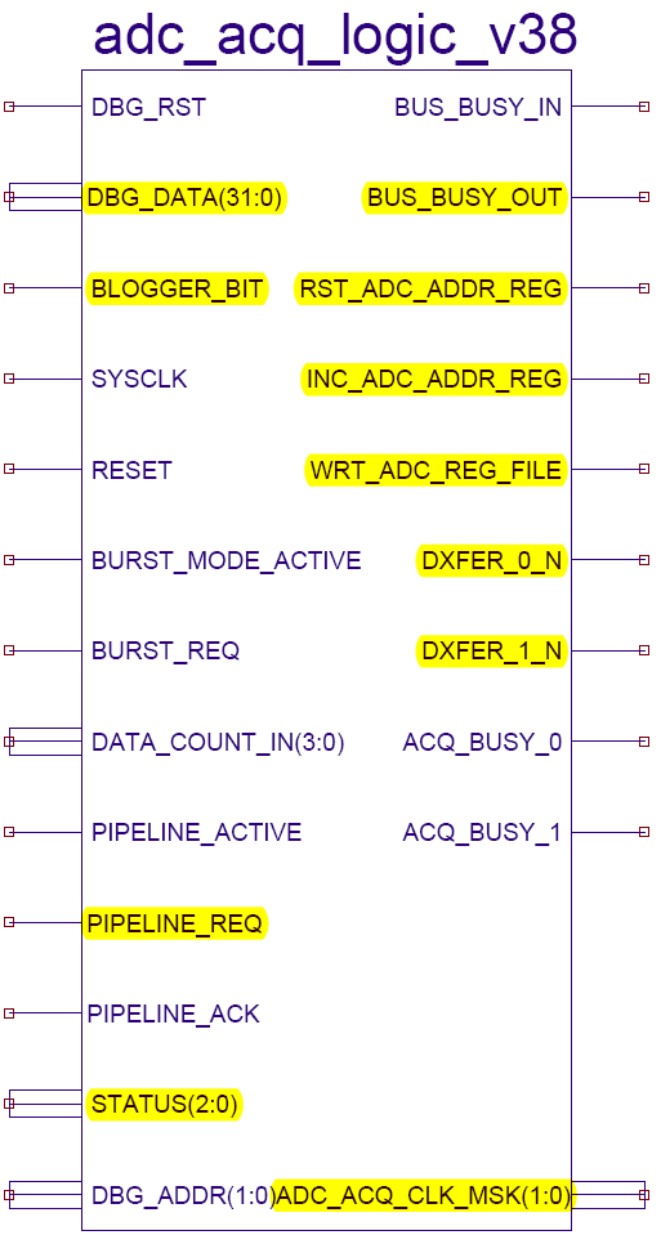
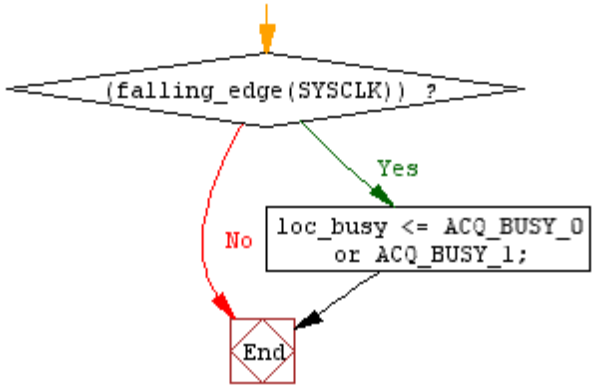
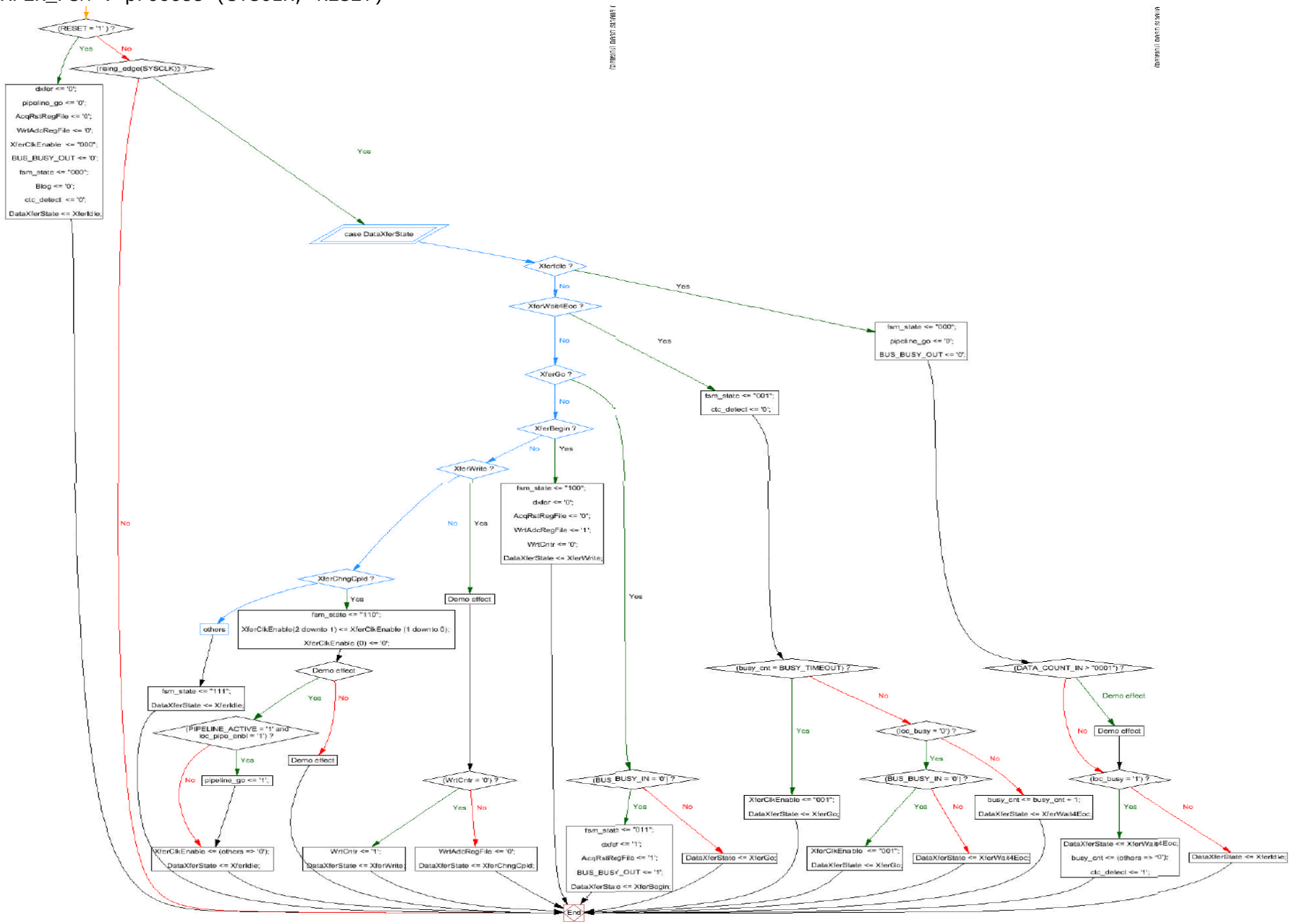


ACQ_BUSY_0 : in std_logic;
ACQ_BUSY_1 : in std_logic;
ADC_ACQ_CLK_MSK : out std_logic_vector(1 downto 0);
BLOGGER_BIT : out std_logic;
BURST_MODE_ACTIVE : in std_logic;
BURST_REQ : in std_logic;
BUS_BUSY_IN : in std_logic;
BUS_BUSY_OUT : out std_logic;
DATA_COUNT_IN : in std_logic_vector(3 downto 0);
DBG_ADDR : in std_logic_vector(1 downto 0);
DBG_DATA : out std_logic_vector(31 downto 0)
DBG_RST : in std_logic;
DXFER_0_N : out std_logic;
DXFER_1_N : out std_logic;
INC_ADC_ADDR_REG : out std_logic;
PIPELINE_ACK : in std_logic;
PIPELINE_ACTIVE : in std_logic;
PIPELINE_REQ : out std_logic;
RESET : in std_logic;
RST_ADC_ADDR_REG : out std_logic;
STATUS : out std_logic_vector(2 downto 0);
SYSCLK : in std_logic;
WRT_ADC_REG_FILE : out std_logic;

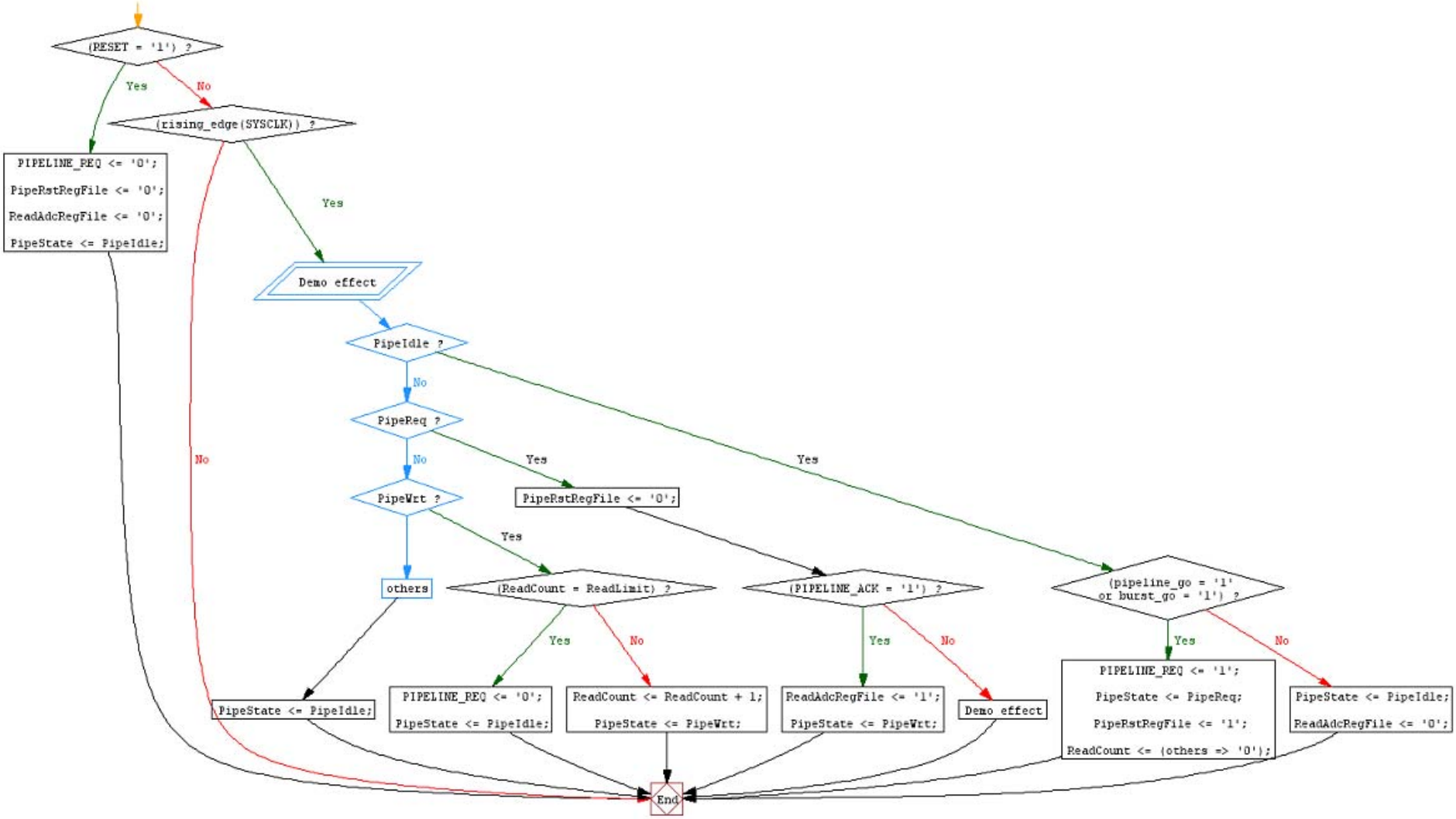
```
BusySyncProc : process(SYSCLK)
```



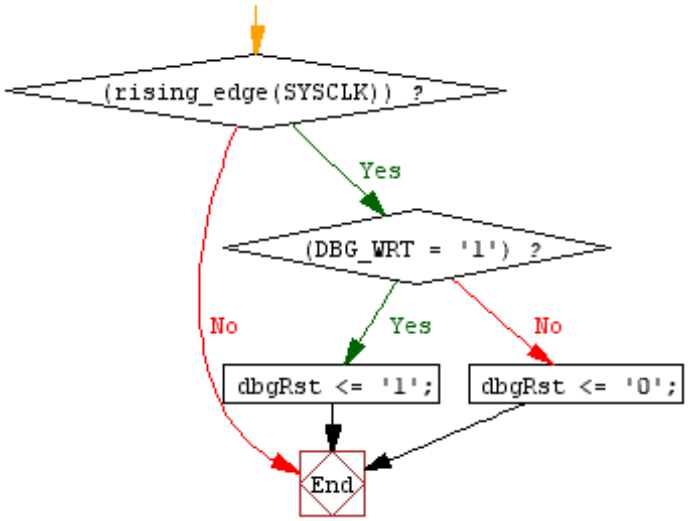
```
XFER_FSM : process (SYSCLK, RESET)
```



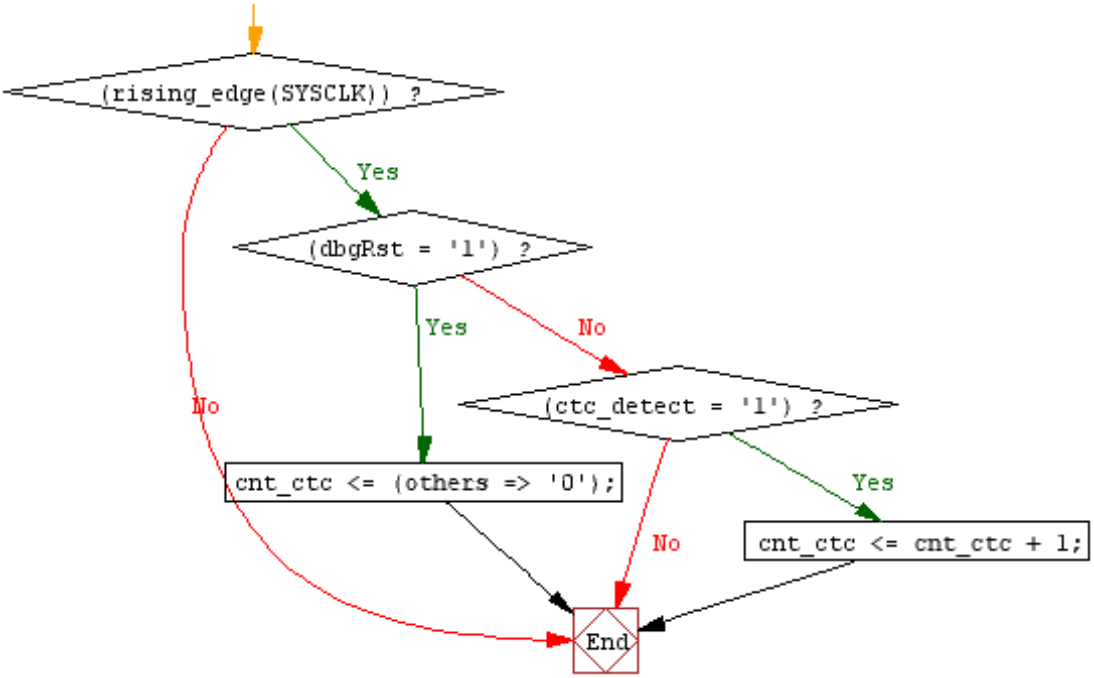
```
PIPE_FSM : process (SYSCLK, RESET)
```



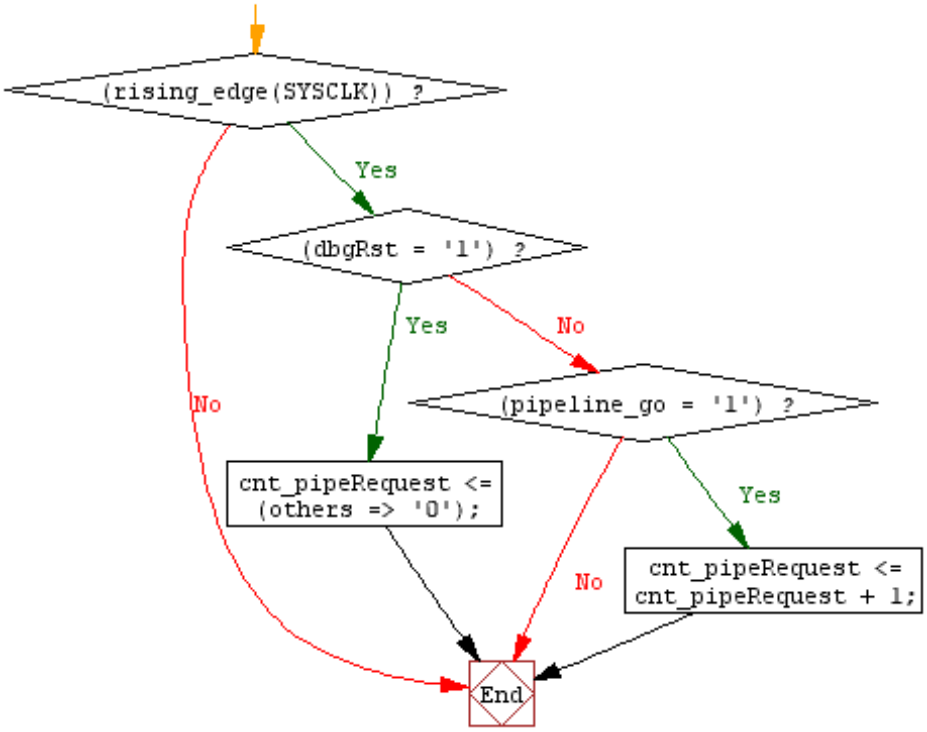
```
DbgRstProc: process (SYSCLK)
```



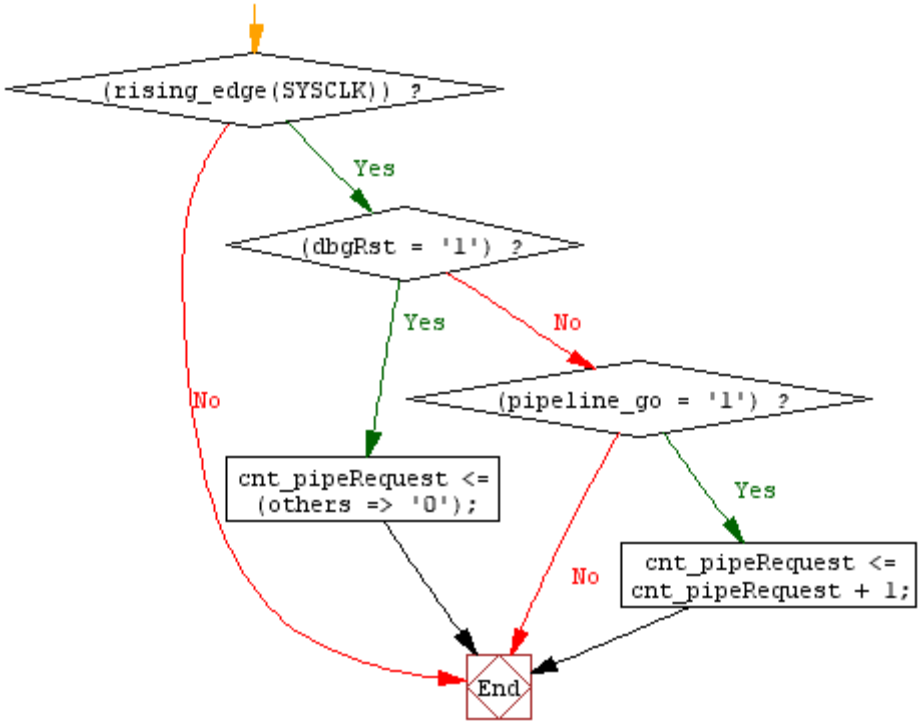

```
CountCtcStrobesProc: process (SYSCLK,dbgRst)
```

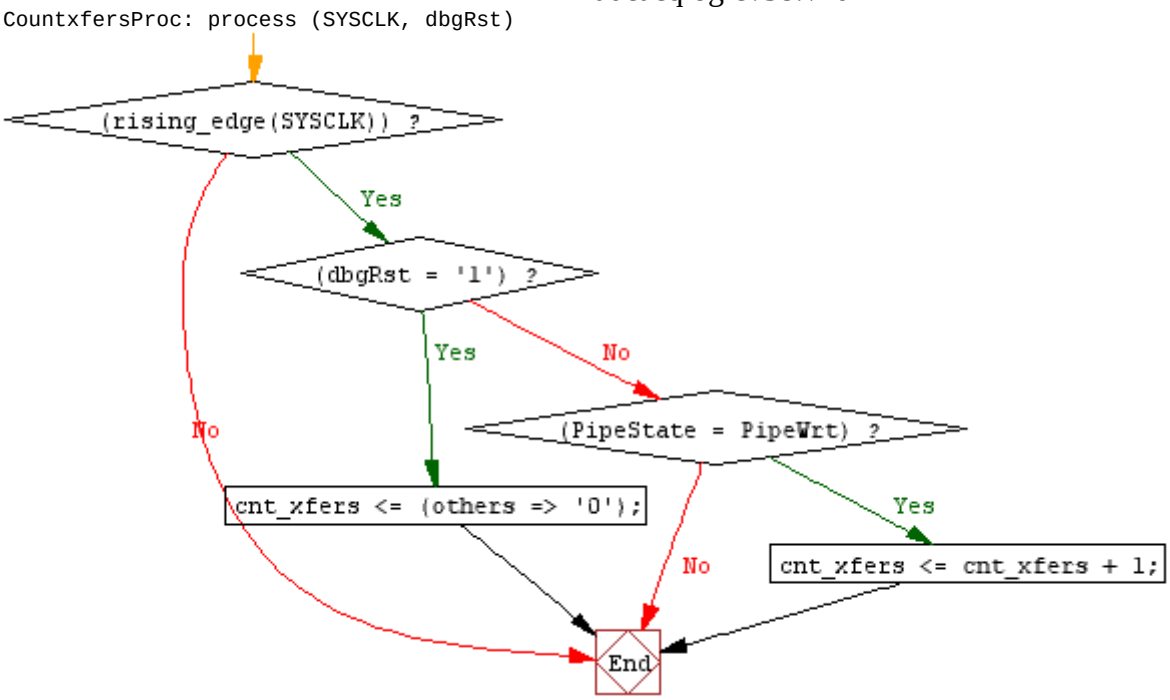


```
CountPipeReqestProc: process (SYSCLK, dbgRst)
```




```
CountxfersProc: process (SYSCLK, dbgRst)
```




CountxfersProc: process (SYSCLK, dbgRst)
(rising_edge(SYSCLK)) ?
Yes
No
(dbgRst = '1') ?
Yes
No
(PipeState = PipeWrt) ?
Yes
No
cnt_xfers <= (others => '0');
cnt_xfers <= cnt_xfers + 1;
End