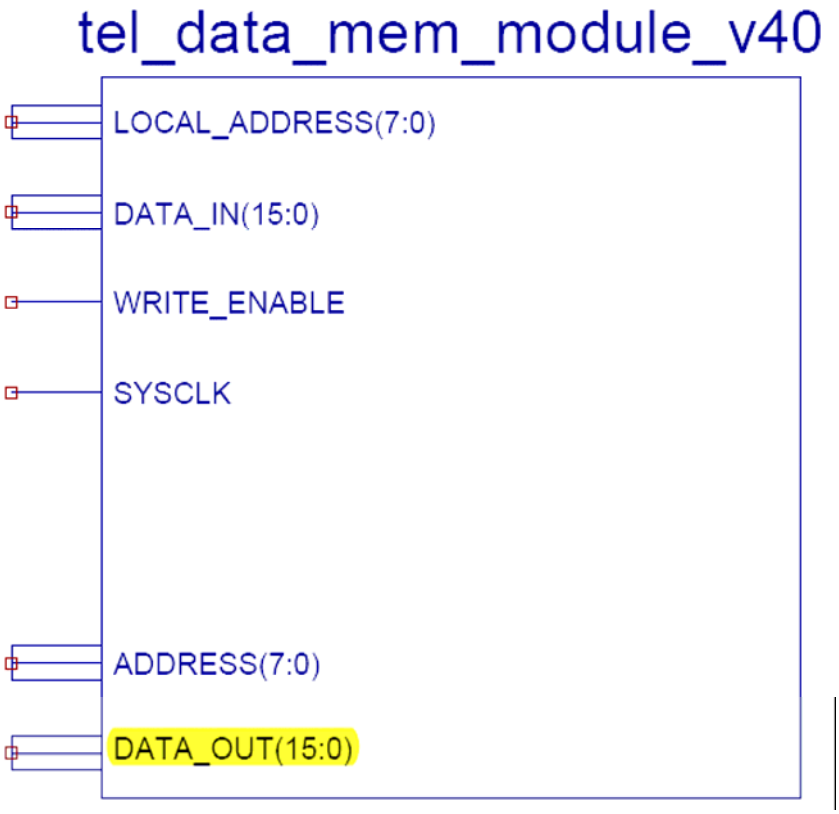
```
-----------------------------------------------------------------------
-- DESCRIPTION  : 256 x 16 bit Ram bank to store the data coming from the
--                telemetry ADCs.
-----------------------------------------------------------------------
```

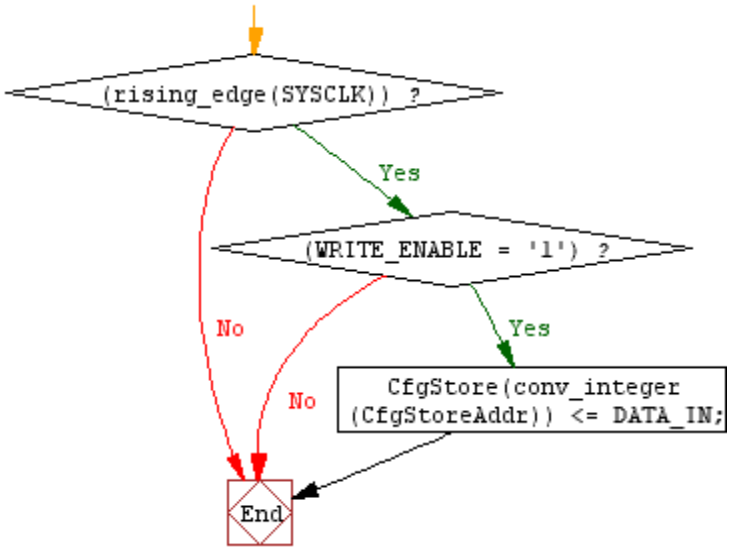


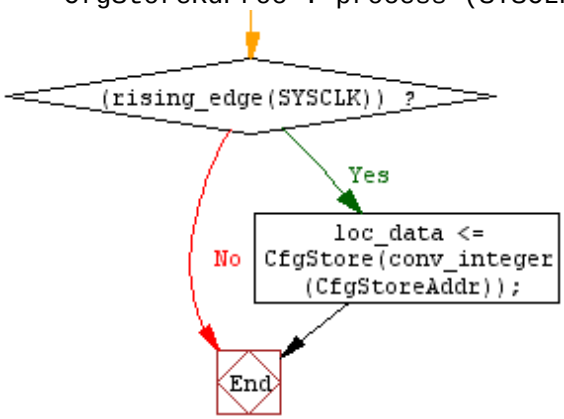

```
ADDRESS        : in  std_logic_vector(7 downto 0);
DATA_IN        : in  std_logic_vector(15 downto 0);
DATA_OUT       : out std_logic_vector(15 downto 0);
LOCAL_ADDRESS  : in  std_logic_vector(7 downto 0);
WRITE_ENABLE   : in  std_logic;
SYSCLK         : in  std_logic

type cfg_ram_type is array (255 downto 0) of std_logic_vector (15 downto 0);
signal CfgStore     : cfg_ram_type;       -- Ram to store Configuration data
signal CfgStoreAddr : std_logic_vector(7 downto 0);
signal loc_data     : std_logic_vector(15 downto 0);

CfgStoreAddr <= LOCAL_ADDRESS when (WRITE_ENABLE = '1') else ADDRESS;
```

```
CfgStoreWrtProc : process (SYSCLK)
```




```
CfgStoreRdProc : process (SYSCLK)
```