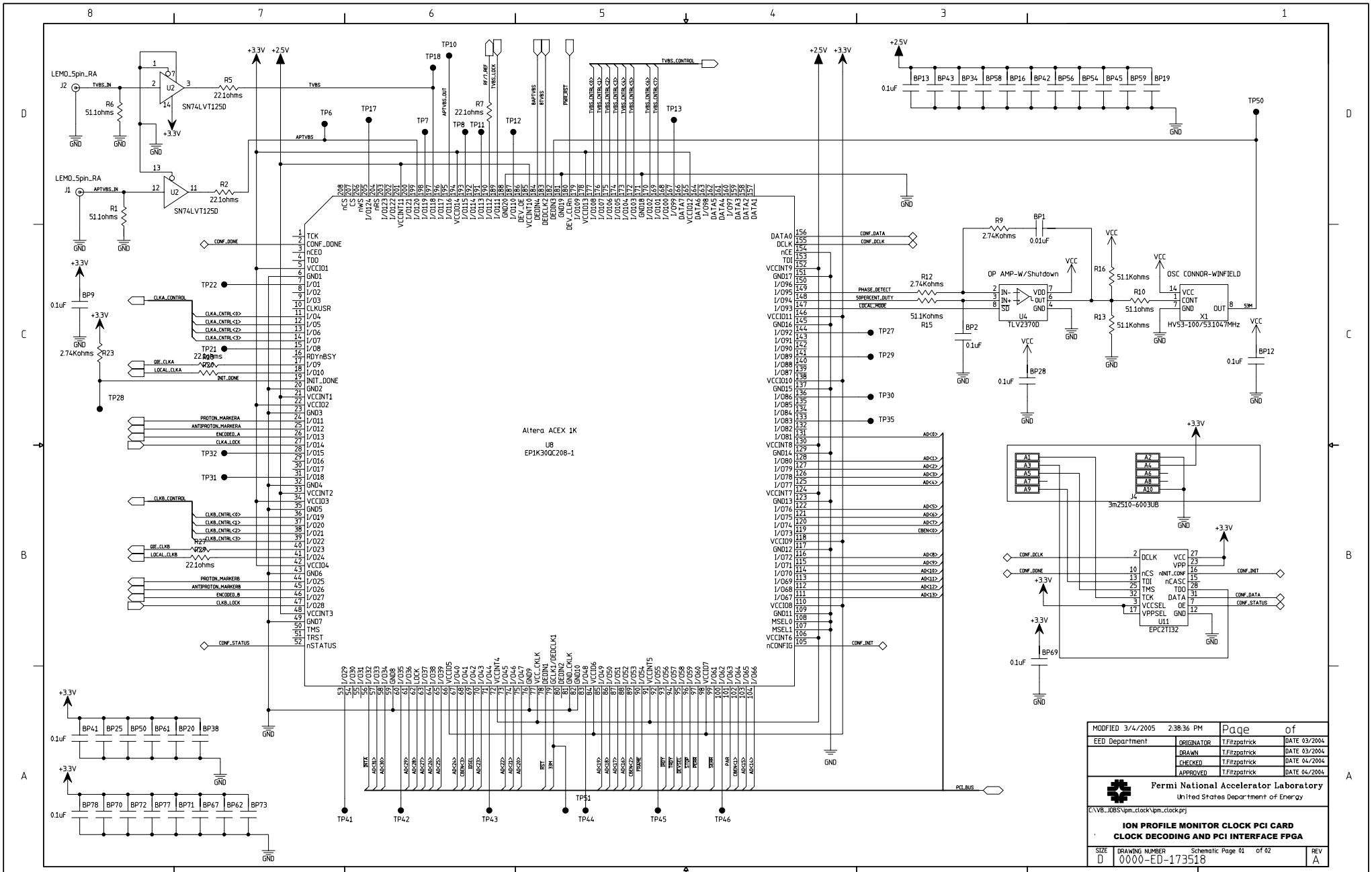
Altera ACEX 1K
U8
EP1K30QC208-1
SN74LVT125D
OP AMP-W/Shutdown
TLV2370D
OSC CONNOR-WINFIELD
HVS3-100/531047MHz
3mZ510-6003UB
EPC2T132
LEMO_5pin_RA
PCI_BUS
TVBS_CONTROL
CLKA_CONTROL
CLKB_CONTROL
CONF_DONE
CONF_STATUS
CONF_DATA
CONF_DCLK
CONF_INIT
INIT_DONE
PHASE_DETECT
50PERCENT_DUTY
LOCAL_MODE
MODIFIED 3/4/2005 2:38:36 PM
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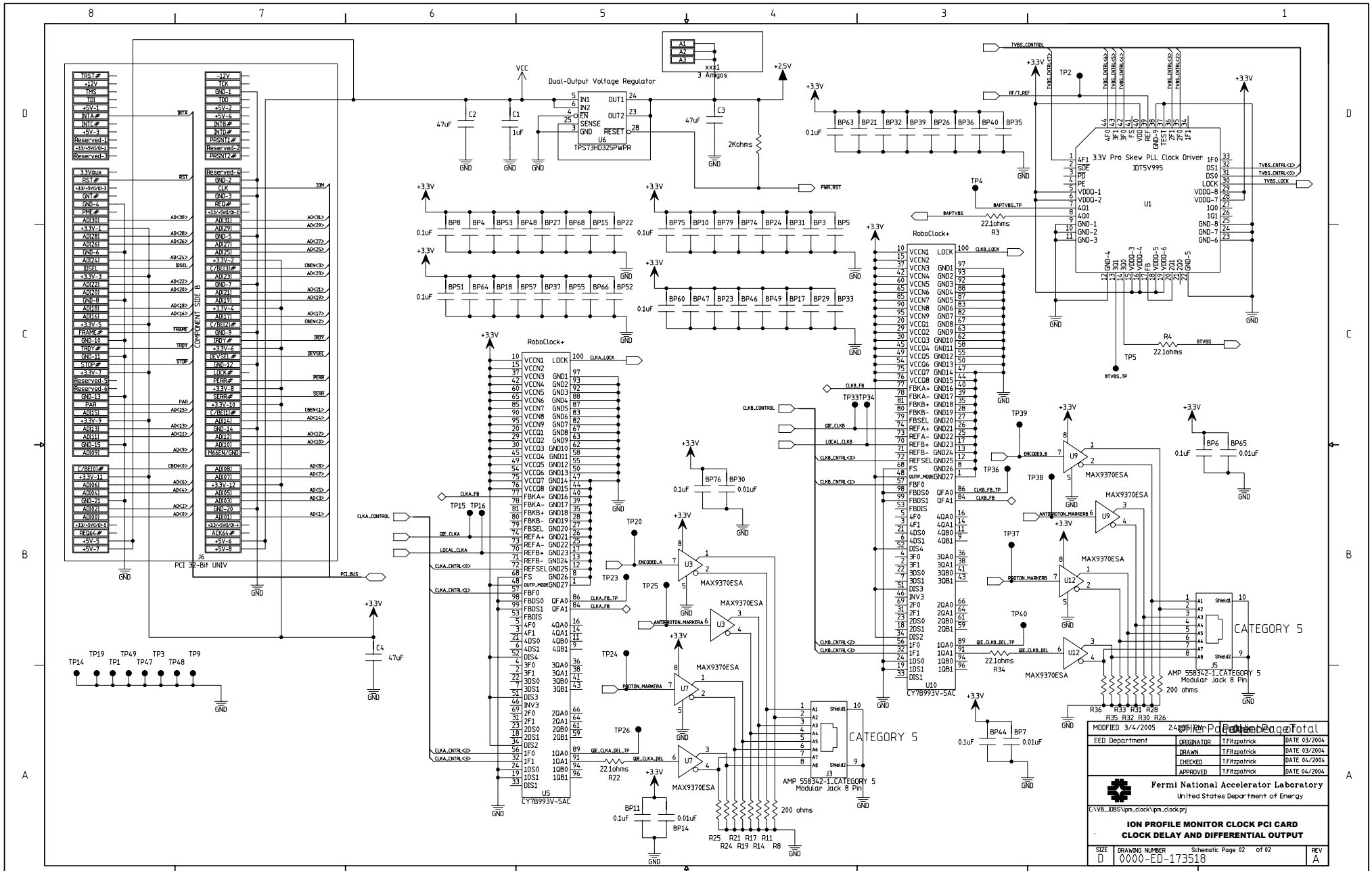
EED Department
ORIGINATOR T.Fitzpatrick DATE 03/2004
DRAWN T.Fitzpatrick DATE 03/2004
CHECKED T.Fitzpatrick DATE 04/2004
APPROVED T.Fitzpatrick DATE 04/2004
Fermi National Accelerator Laboratory
United States Department of Energy
C:\VB_JOBS\ipm_clock\ipm_clock.prj
ION PROFILE MONITOR CLOCK PCI CARD
CLOCK DECODING AND PCI INTERFACE FPGA
SIZE D
DRAWING NUMBER 0000-ED-173518
Schematic Page 01 of 02
REV A


Dual-Output Voltage Regulator
U6
TPS73HD325PWPR
3 Amigos
PCI 32-Bit UNIV
J6
COMPONENT SIDE B
3.3V Pro Skew PLL Clock Driver
IDT5V995
U1
RoboClock+
U5
CY7B993V-5AC
U10
CY7B993V-5AC
MAX9370ESA
CATEGORY 5
AMP 558342-1 CATEGORY 5
Modular Jack 8 Pin
J3
J5
200 ohms
ION PROFILE MONITOR CLOCK PCI CARD
CLOCK DELAY AND DIFFERENTIAL OUTPUT
Fermi National Accelerator Laboratory
United States Department of Energy
EED Department
ORIGINATOR T.Fitzpatrick DATE 03/2004
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Schematic Page 02 of 02
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