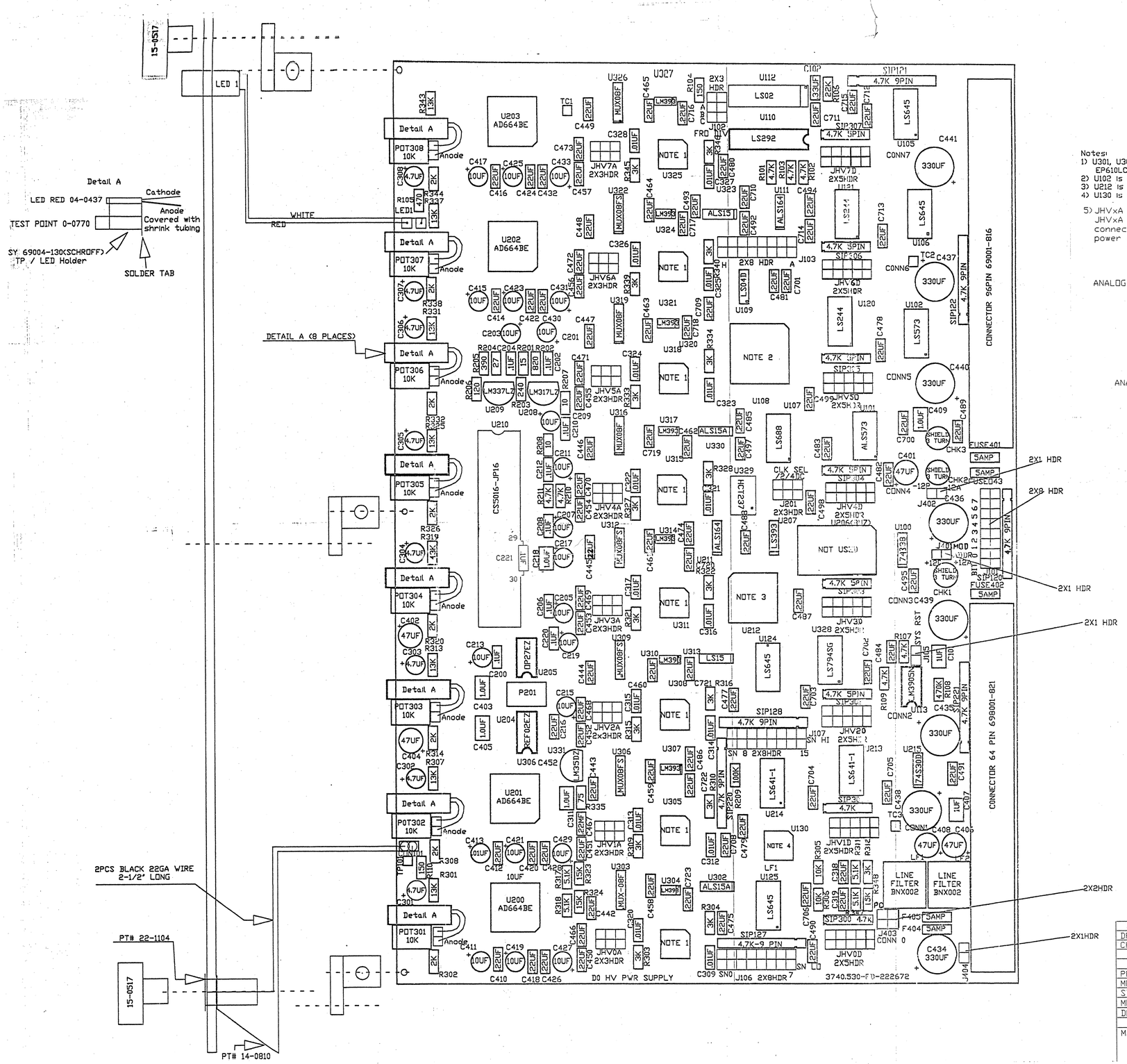
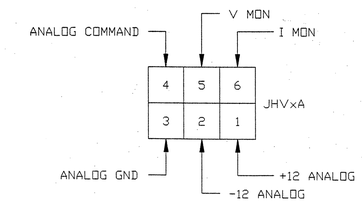


Notes:
1) U301, U305, U308, U311, U315, U318, U321, U325 is a EP610LC-30 Programmed with PODINLOC.JED.
2) U102 is a PLHSS01A Programmed with CONTROL.JED.
3) U212 is a EP910LC-30 Programmed with CLKDEC.JED.
4) U130 is a EP610LC-30 Programmed with MODINTLC.JED.

5) JHVxA and JHVxD pinouts are as shown below. JHVxA is the analog signal and analog power connector. JHVxD is the digital signal and bulk power connector. x denotes channel number.



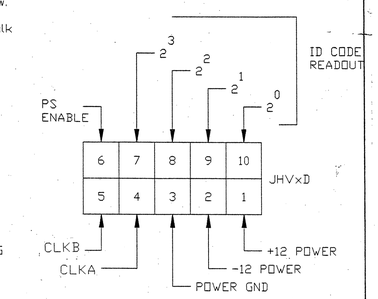


Figure 5

DRAWN	BY ANDRZEJ	Bi Ra Systems Inc.
CHECKED		
APPROVALS		VME 4877 MOTHER BOARD COMPONENT LAYOUT
PROJECT		
MFG		
STRESS		
MET		
DRB		
MATERIAL:		D 6/11/96 DWG. NO. Figure 5 REV. A
		1:1 MECHANICAL DWG. sheet 1 of 1