



SSR R&D

- Summary of activities
 - Review literature
 - Contact vendors / test lab contacts
 - Perform study of state of the art FLASH
 - Future study of DRAM
- Deliverables
 - SSR specifications document
 - Contribution to CDR
 - Contribution to procurement of the SSR
 - Includes procurement
 - Includes test and qualifications



Methods

- Literature review
 - Internet/library documentation
 - Front-end electronics and NSREC conferences
 - Produce a table of existing and planned SSRs
- Vendor/test lab contacts
 - Initial contact made with SEKR
 - Initial contact made with JPL
- SSR specification document version 0 exists and will be modified



Flash R&D

- Procure samples (can be done ASAP)
- Design evaluation printed circuit board
 - Based on existing design
 - Rely on software for smarts
- Software
 - Low level is based on existing efforts
 - High level is to control and exercise Flash
- Produce and test board by July
- Work with Flash until September
- Target September for radiation tests