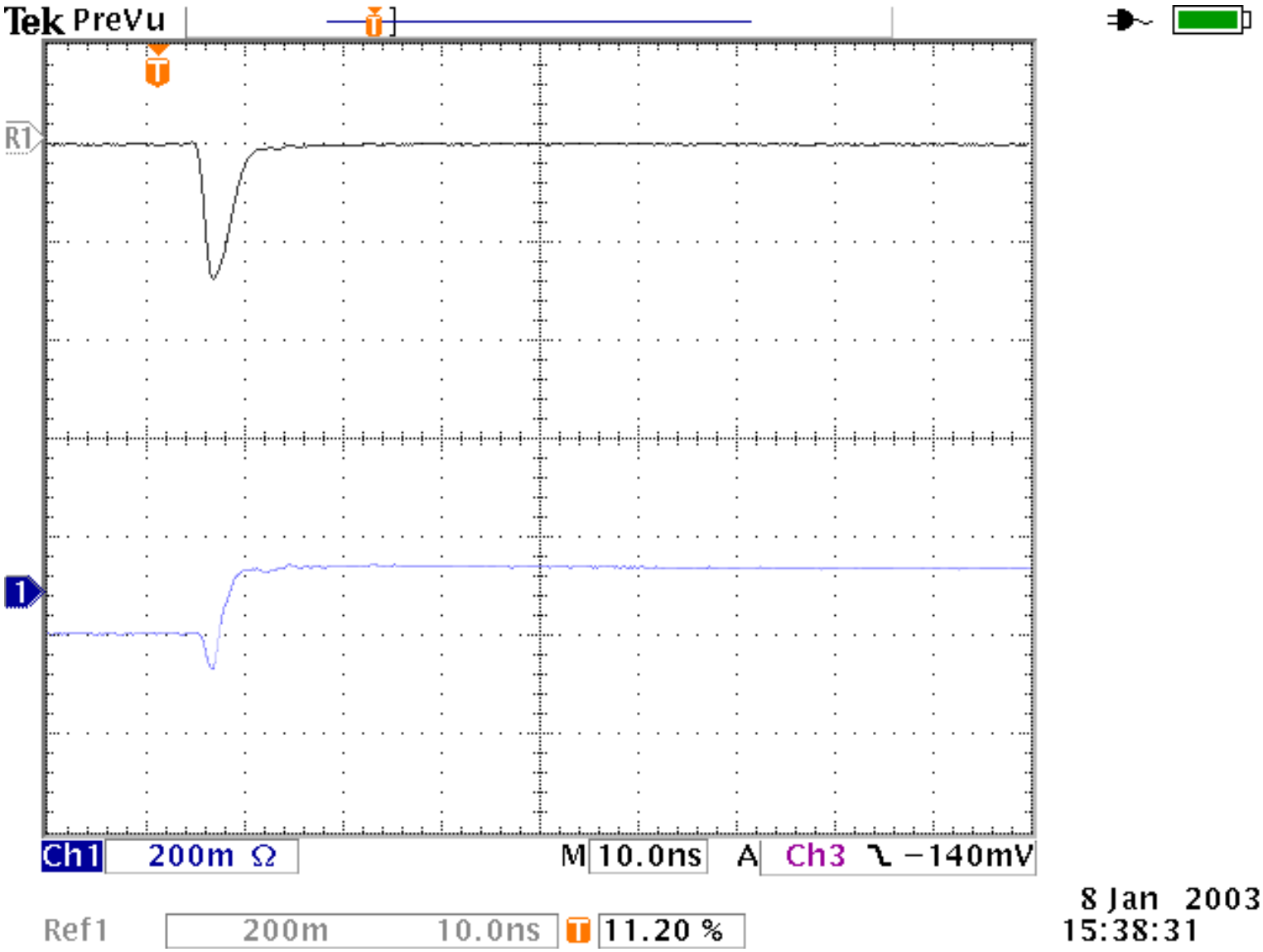
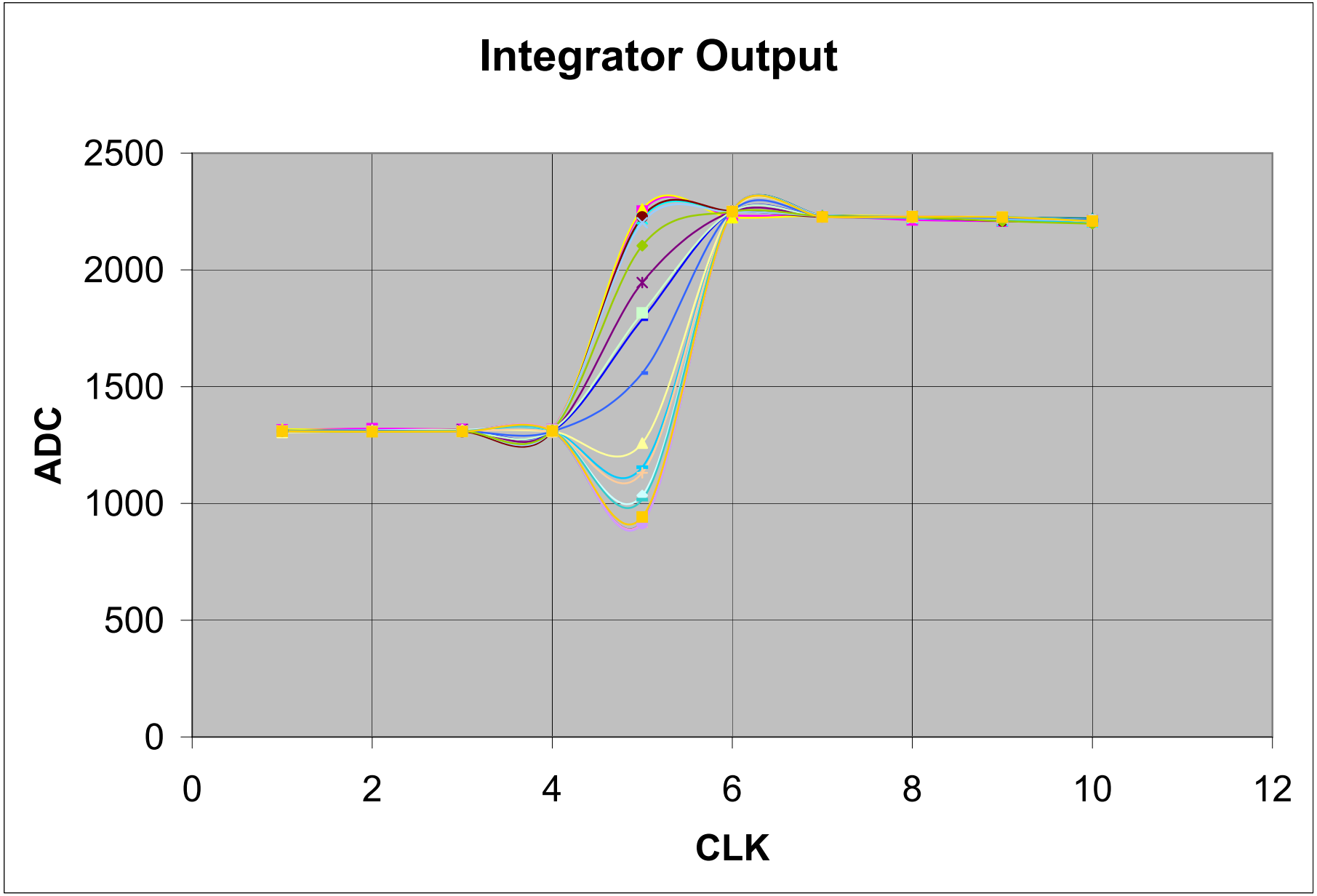
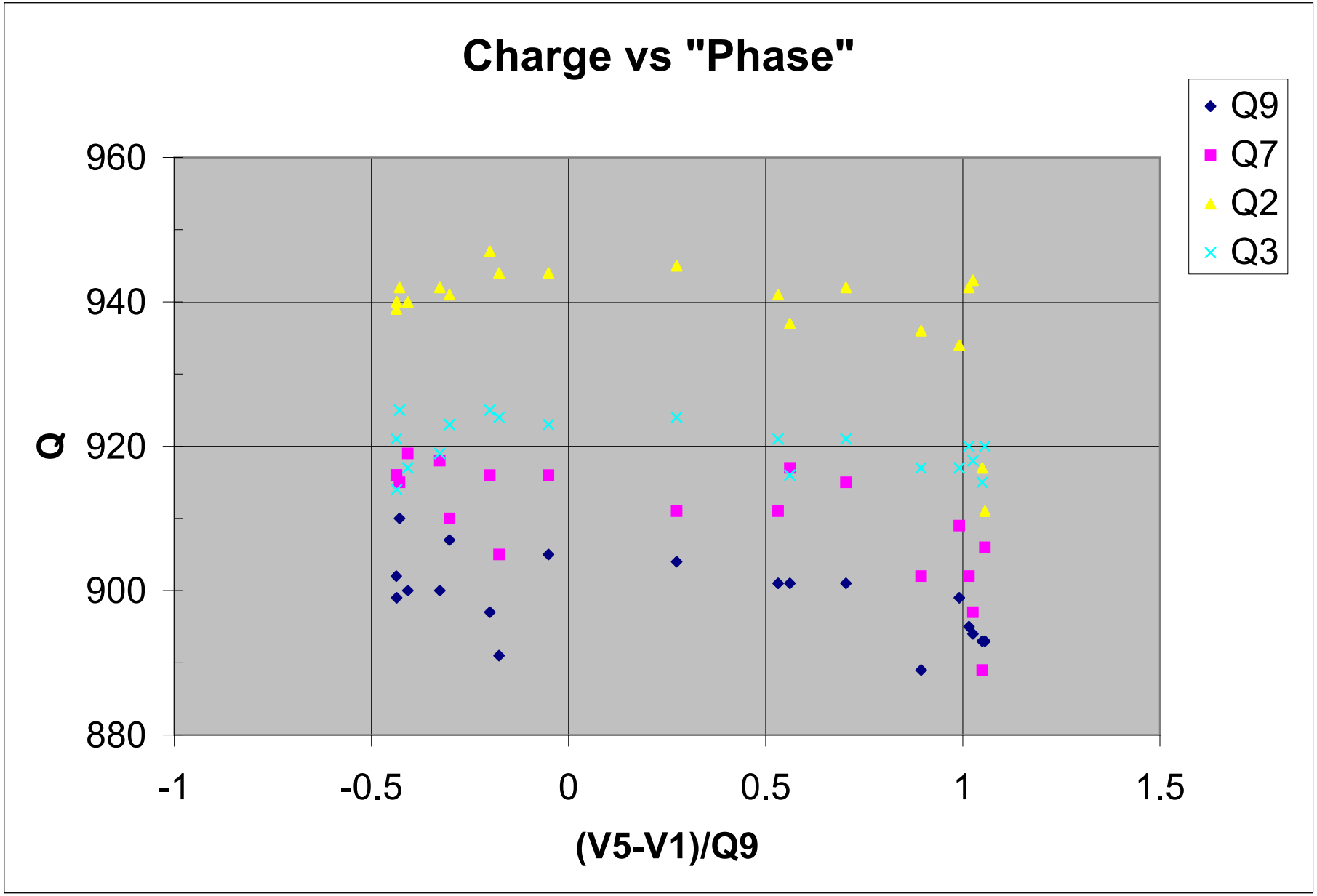
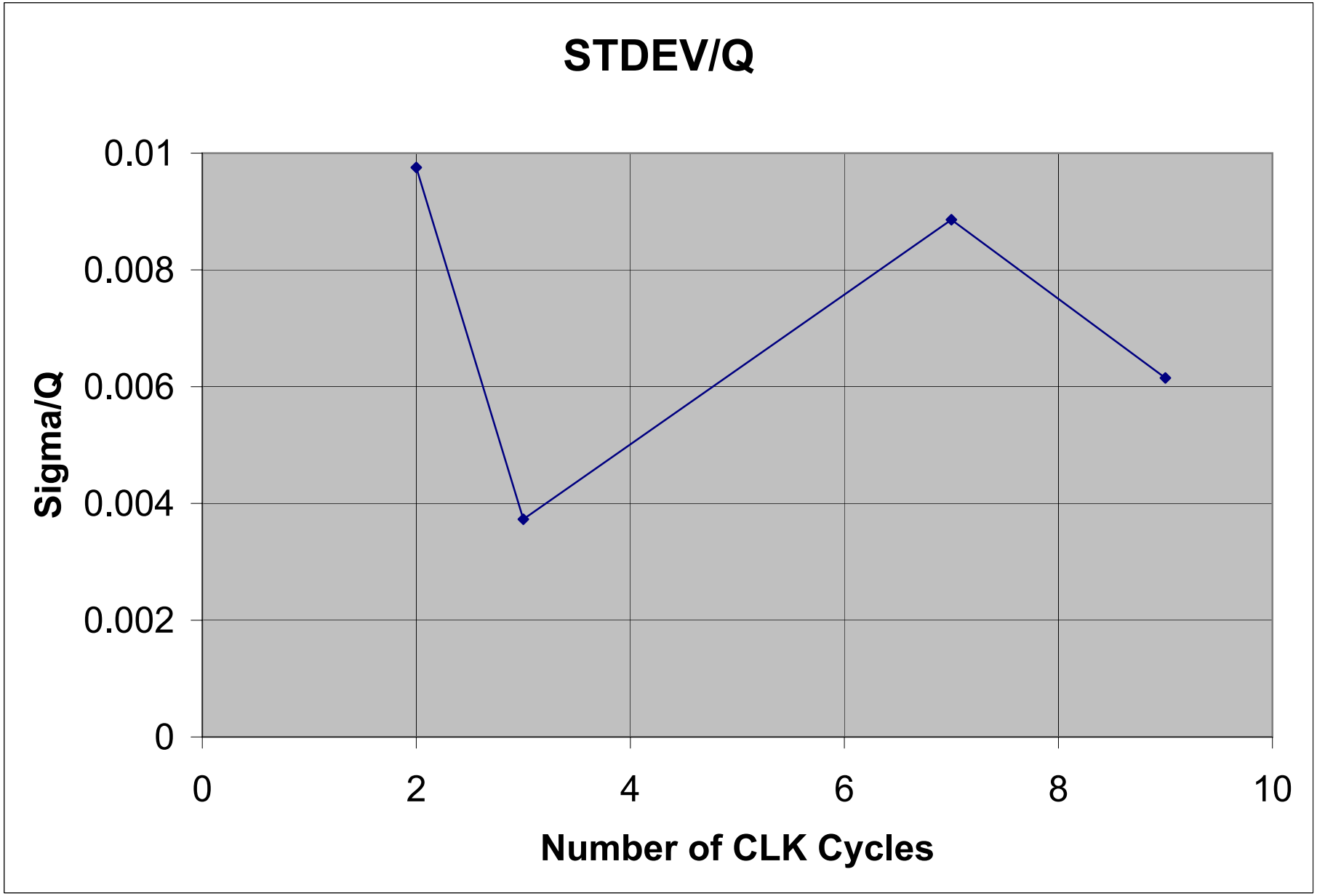

Tek PreVu
R1
1
Ch1 200m Ω
M 10.0ns A Ch3 ⌐ −140mV
Ref1 200m 10.0ns
11.20 %
8 Jan 2003
15:38:31


Integrator Output
2500
2000
1500
1000
500
0
ADC
0
2
4
6
8
10
12
CLK


Charge vs "Phase"
Q9
Q7
Q2
Q3
960
940
920
900
880
Q
-1
-0.5
0
0.5
1
1.5
(V5-V1)/Q9


STDEV/Q
0.01
0.008
0.006
0.004
0.002
0
Sigma/Q
0
2
4
6
8
10
Number of CLK Cycles