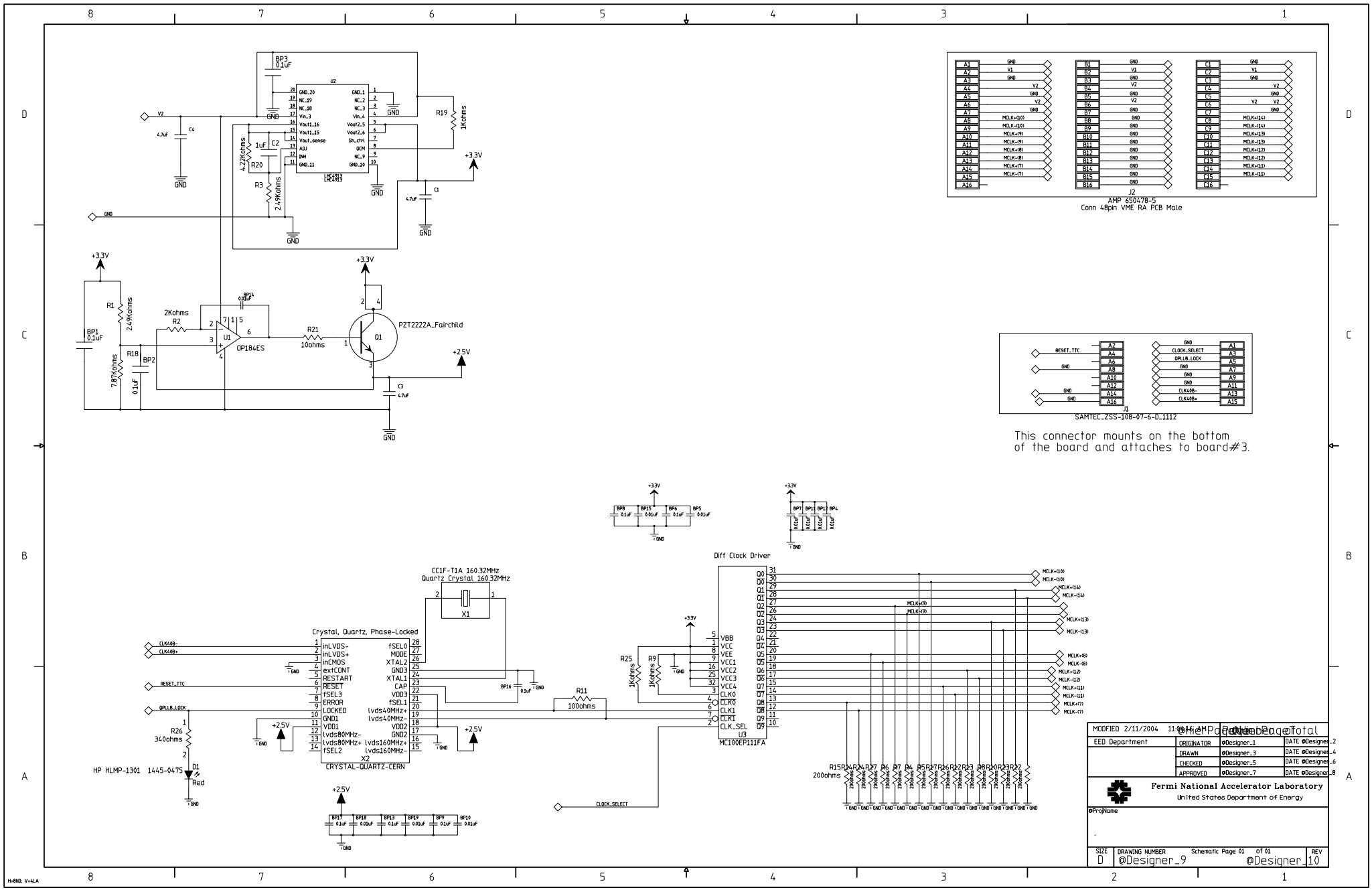

U2
LHC4913
LHC4913
BP3
0.1uF
GND
V2
C4
4.7uF
R19
1Kohms
+3.3V
C2
1uF
R20
4.22Kohms
R3
2.49Kohms
C1
4.7uF
GND
+3.3V
R1
2.49Kohms
BP1
0.1uF
R18
7.87Kohms
BP2
0.1uF
2Kohms
R2
BP14
0.01uF
U1
OP184ES
R21
10ohms
Q1
PZT2222A_Fairchild
+2.5V
C3
4.7uF
J2
AMP 650478-5
Conn 48pin VME RA PCB Male
J1
SAMTEC_ZSS-108-07-6-D_1112
This connector mounts on the bottom
of the board and attaches to board#3.
Diff Clock Driver
CC1F-T1A 160.32MHz
Quartz Crystal 160.32MHz
X1
Crystal, Quartz, Phase-Locked
inLVDS-
inLVDS+
inCMOS
extCONT
RESTART
RESET
fSEL3
ERROR
LOCKED
GND1
VDD1
lvds80MHz-
lvds80MHz+
fSEL2
fSEL0
MODE
XTAL2
GND3
XTAL1
CAP
VDD3
fSEL1
lvds40MHz+
lvds40MHz-
VDD2
GND2
lvds160MHz+
lvds160MHz-
X2
CRYSTAL-QUARTZ-CERN
CLK40B-
CLK40B+
RESET_TTC
QPLLB_LOCK
R26
340ohms
D1
Red
HP HLMP-1301 1445-0475
R11
100ohms
R25
1Kohms
R9
1Kohms
VBB
VCC
VEE
VCC1
VCC2
VCC3
VCC4
CLK0
CLK0
CLK1
CLK1
CLK_SEL
U3
MC100EP111FA
CLOCK_SELECT
R15R24R24R27 R6 R7 R4 R5R27R26R22R23 R8R20R23R22
200ohms
MCLK+(10)
MCLK-(10)
MCLK+(14)
MCLK-(14)
MCLK+(9)
MCLK-(9)
MCLK+(13)
MCLK-(13)
MCLK+(8)
MCLK-(8)
MCLK+(12)
MCLK-(12)
MCLK+(11)
MCLK-(11)
MCLK+(7)
MCLK-(7)
MODIFIED 2/11/2004 11:06:14 AM
EED Department
ORIGINATOR @Designer_1
DRAWN @Designer_3
CHECKED @Designer_5
APPROVED @Designer_7
DATE @Designer_2
DATE @Designer_4
DATE @Designer_6
DATE @Designer_8
Fermi National Accelerator Laboratory
United States Department of Energy
@ProjName
SIZE D
DRAWING NUMBER @Designer_9
Schematic Page 01 of 01
REV @Designer_10
H=8ND; V=4LA