

CCA Register Settings - CCA latency is 3 clock cycles

QIE Delay	DLL Tap for QIE Register		Alignment Control of QIE Register	
	Digital	Hex	Digital	Hex
0	0	0x0	17	0x11
1	1	0x1	17	0x11
2	2	0x2	17	0x11
3	3	0x3	17	0x11
4	4	0x4	17	0x11
5	5	0x5	17	0x11
6	6	0x6	17	0x11
7	7	0x7	17	0x11
8	8	0x8	17	0x11
9	9	0x9	17	0x11
10	10	0xA	17	0x11
11	11	0xB	17	0x11
12	12	0xC	17	0x11
13	13	0xD	17	0x11
14	14	0xE	2	0x02
15	15	0xF	2	0x02
16	16	0x10	2	0x02
17	17	0x11	2	0x02
18	18	0x12	2	0x02
19	19	0x13	2	0x02
20	20	0x14	2	0x02
21	21	0x15	2	0x02
22	22	0x16	2	0x02
23	23	0x17	2	0x02
24	24	0x18	2	0x02
25	0	0x0	2	0x02
26	1	0x1	2	0x02