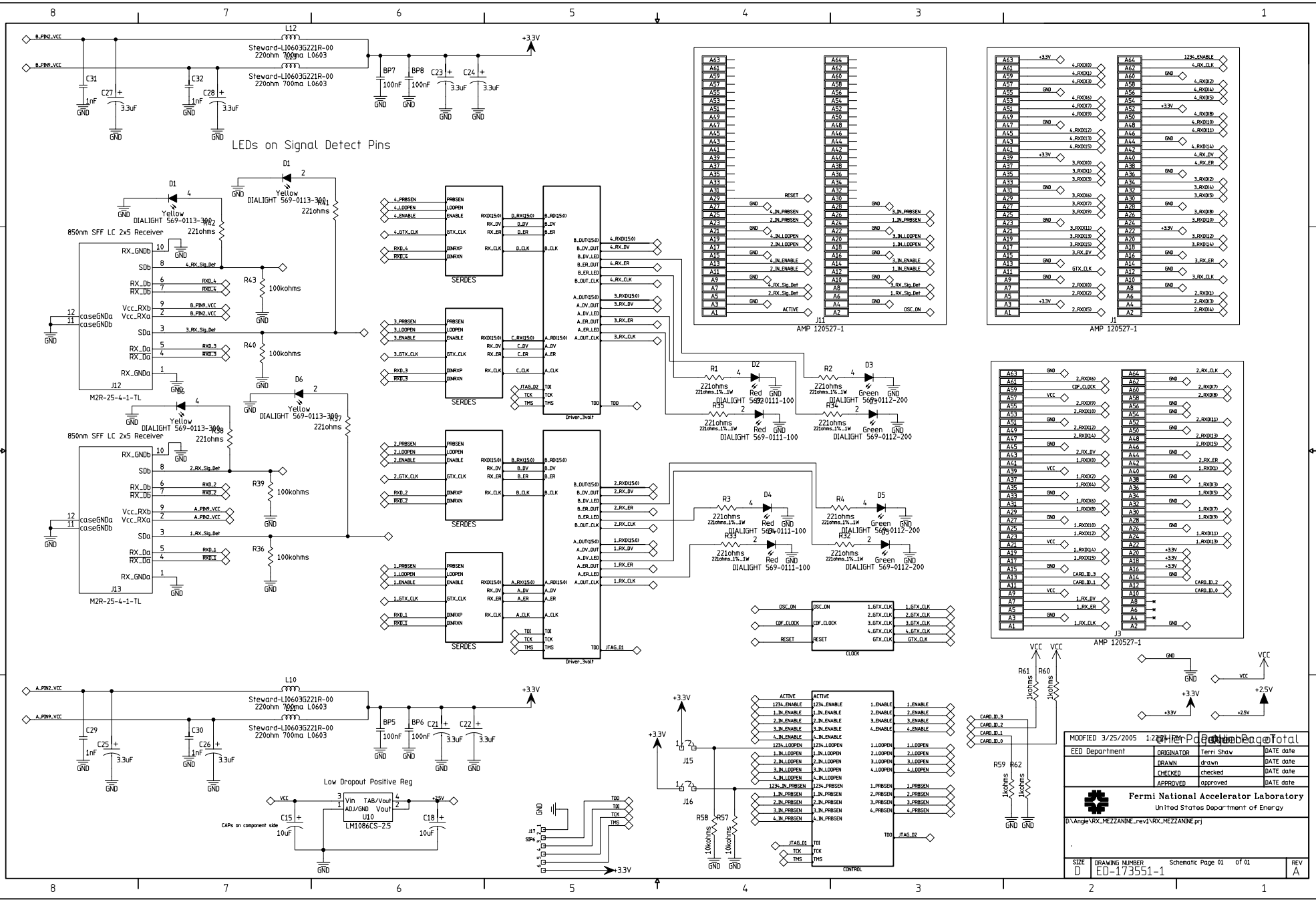
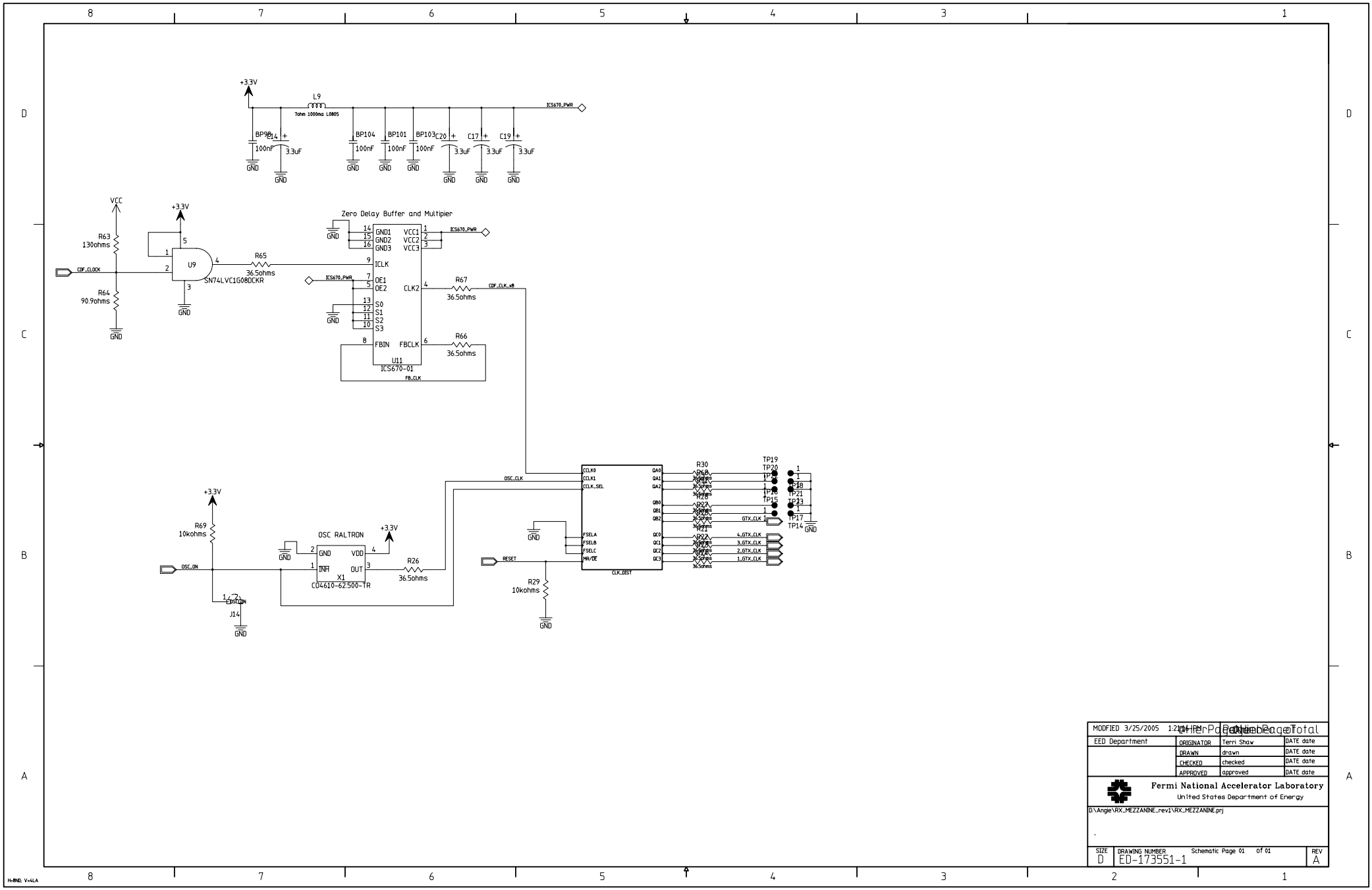

LEDs on Signal Detect Pins
850nm SFF LC 2x5 Receiver
M2R-25-4-1-TL
SERDES
Driver_3volt
AMP 120527-1
CLOCK
CONTROL
Low Dropout Positive Reg
LM1086CS-2.5
Steward-LI0603G221R-00
220ohm 700ma L0603
MODIFIED 3/25/2005
EED Department
Terri Shaw
Fermi National Accelerator Laboratory
United States Department of Energy
D:\Angie\RX_MEZZANINE_rev1\RX_MEZZANINE.prj
ED-173551-1
Schematic Page 01 of 01



+3.3V
L9
7ohm 1000ma L0805
ICS670_PWR
BP98
C14
100nF
3.3uF
BP104
BP101
BP103
C20
C17
C19
GND
VCC
R63
130ohms
R64
90.9ohms
CDF_CLOCK
U9
SN74LVC1G08DCKR
R65
36.5ohms
Zero Delay Buffer and Multipier
GND1
GND2
GND3
VCC1
VCC2
VCC3
ICLK
OE1
OE2
CLK2
S0
S1
S2
S3
FBIN
FBCLK
U11
ICS670-01
FB_CLK
R67
36.5ohms
CDF_CLK_x8
R66
36.5ohms
R69
10kohms
OSC_ON
OSC RALTRON
GND
VDD
INH
OUT
X1
CO4610-62.500-TR
R26
36.5ohms
OSC_CLK
CCLK0
CCLK1
CCLK_SEL
FSELA
FSELB
FSELC
MR/OE
QA0
QA1
QA2
QB0
QB1
QB2
QC0
QC1
QC2
QC3
CLK_DIST
RESET
R29
10kohms
R30
R28
R27
R25
R24
R22
R23
R21
R20
R19
R18
R17
R16
R15
TP19
TP20
TP21
TP23
TP17
TP14
GTX_CLK
4_GTX_CLK
3_GTX_CLK
2_GTX_CLK
1_GTX_CLK
J14
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EED Department
ORIGINATOR Terri Shaw
DRAWN drawn
CHECKED checked
APPROVED approved
DATE date
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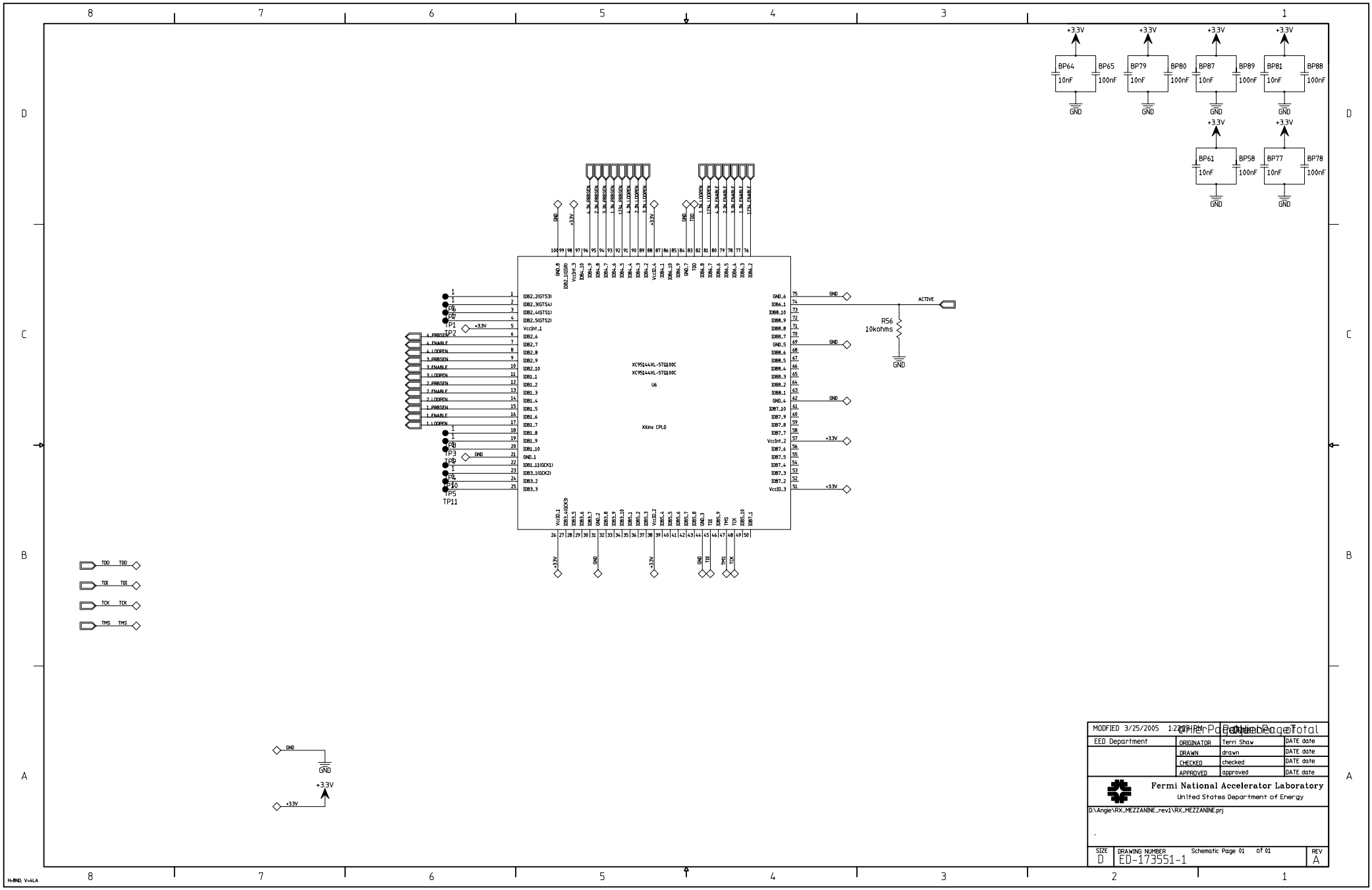
SIZE D
DRAWING NUMBER ED-173551-1
Schematic Page 01 of 01
REV A
H=BND; V=4LA



XC95144XL-5TQ100C
XC95144XL-5TQ100C
U6
Xilinx CPLD
ACTIVE
R56
10kohms
GND
+3.3V
BP64
BP65
BP79
BP80
BP87
BP89
BP81
BP88
BP61
BP58
BP77
BP78
10nF
100nF
TP1
TP2
TP3
TP5
TP11
TDO
TDI
TCK
TMS
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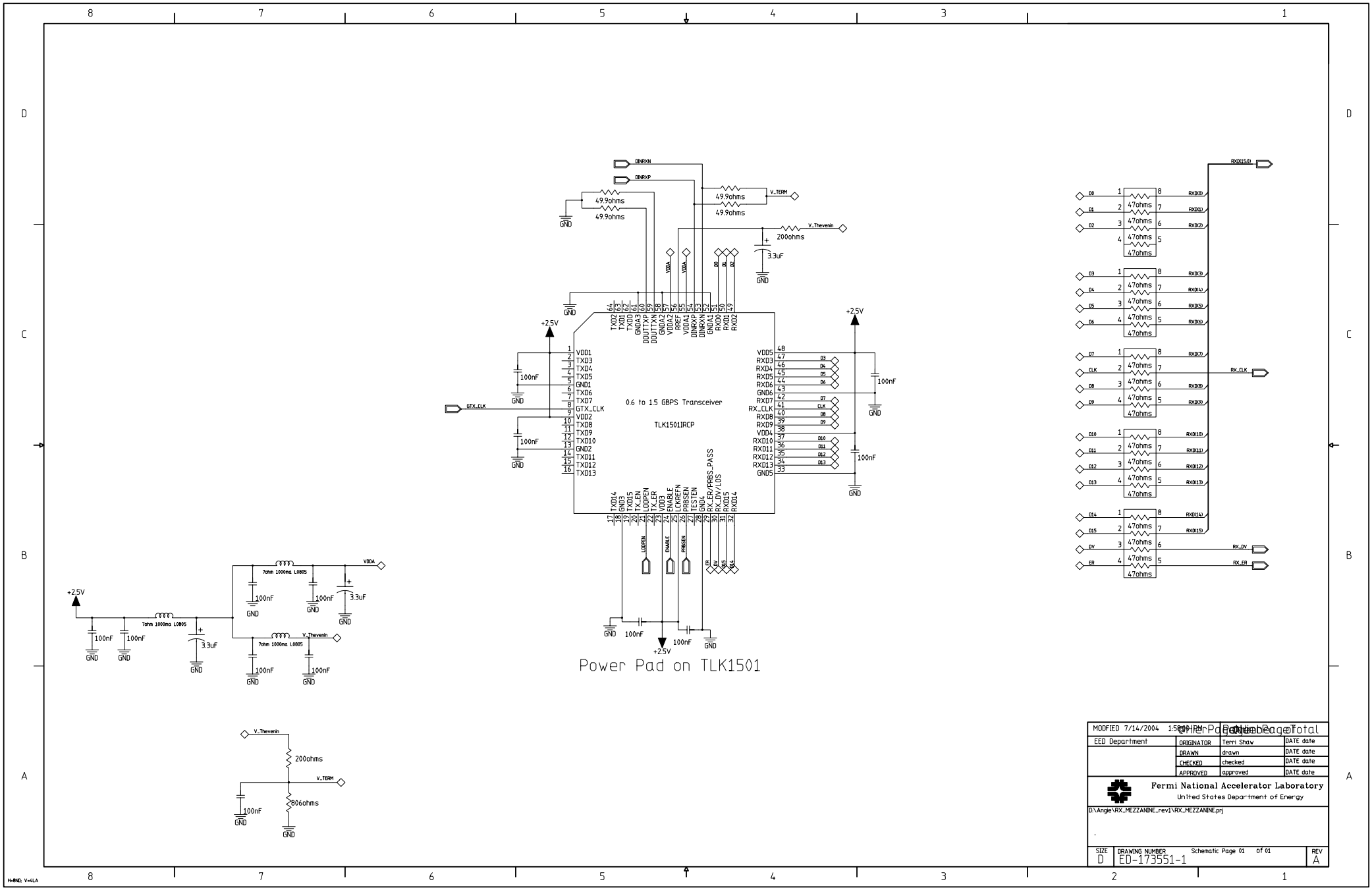
Schematic Page 01 of 01
REV
A
H=BND; V=4LA



0.6 to 1.5 GBPS Transceiver
TLK1501IRCP
Power Pad on TLK1501
DINRXN
DINRXP
49.9ohms
V_TERM
V_Thevenin
200ohms
3.3uF
GND
+2.5V
100nF
GTX_CLK
VDD1
TXD3
TXD4
TXD5
GND1
TXD6
TXD7
GTX_CLK
VDD2
TXD8
TXD9
TXD10
GND2
TXD11
TXD12
TXD13
VDD5
RXD3
RXD4
RXD5
RXD6
GND6
RXD7
RX_CLK
RXD8
RXD9
VDD4
RXD10
RXD11
RXD12
RXD13
GND5
TXD14
GND3
TXD15
TX_EN
LOOPEN
TX_ER
VDD3
ENABLE
LCKREFN
PRBSEN
TESTEN
GND4
RX_ER/PRBS_PASS
RX_DV/LOS
RXD15
RXD14
LOOPEN
ENABLE
PRBSEN
ER
DV
RXD15
RXD14
RXD(15:0)
47ohms
RX_CLK
RX_DV
RX_ER
7ohm 1000ma L0805
VDDA
806ohms
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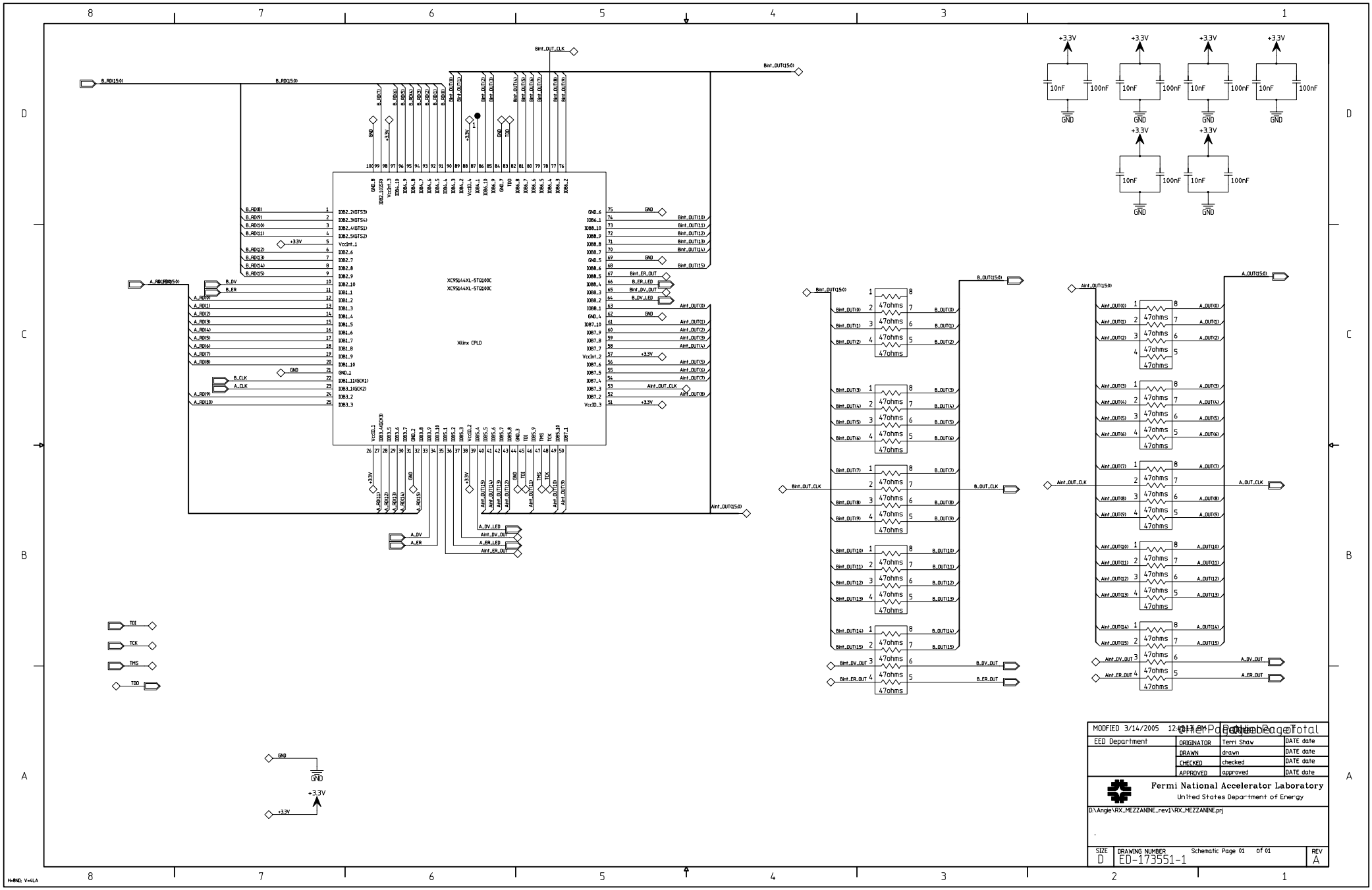
Schematic Page 01 of 01
REV A
H=BND; V=4LA



XC95144XL-5TQ100C
Xilinx CPLD
47ohms
10nF
100nF
+3.3V
GND
TDI
TCK
TMS
TDO
MODIFIED 3/14/2005
EED Department
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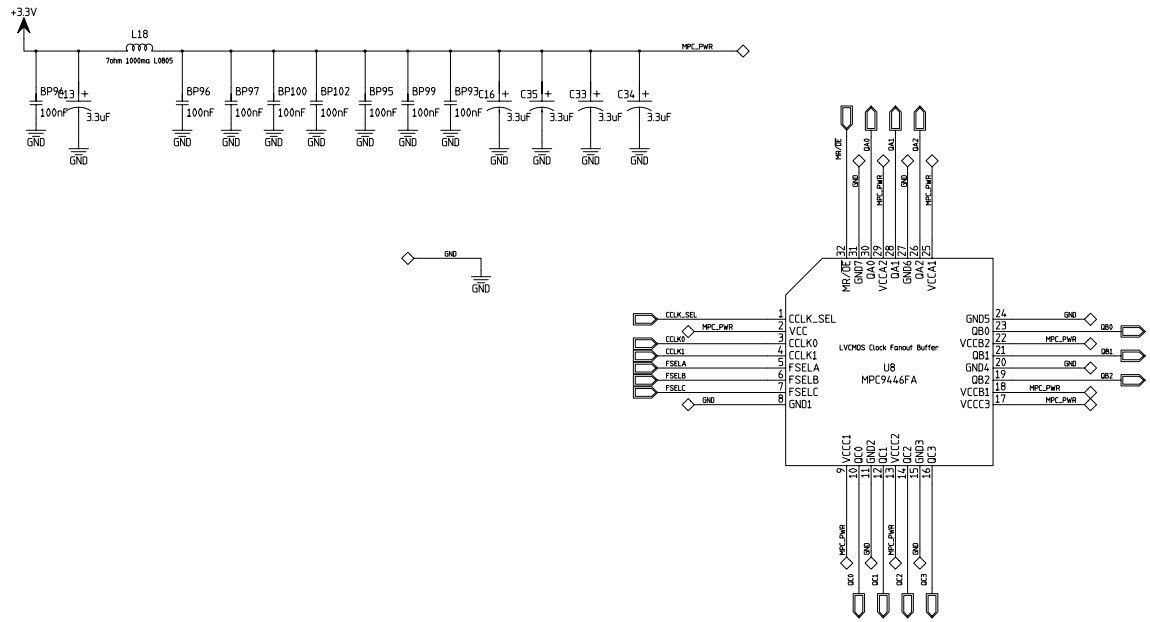
Schematic Page 01 of 01
REV A
H=BND; V=4LA



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	DRAWN	drawn	DATE date
	CHECKED	checked	DATE date
	APPROVED	approved	DATE date

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SIZE	DRAWING NUMBER	Schematic Page 01 of 01	REV
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H=BND; V=4LA