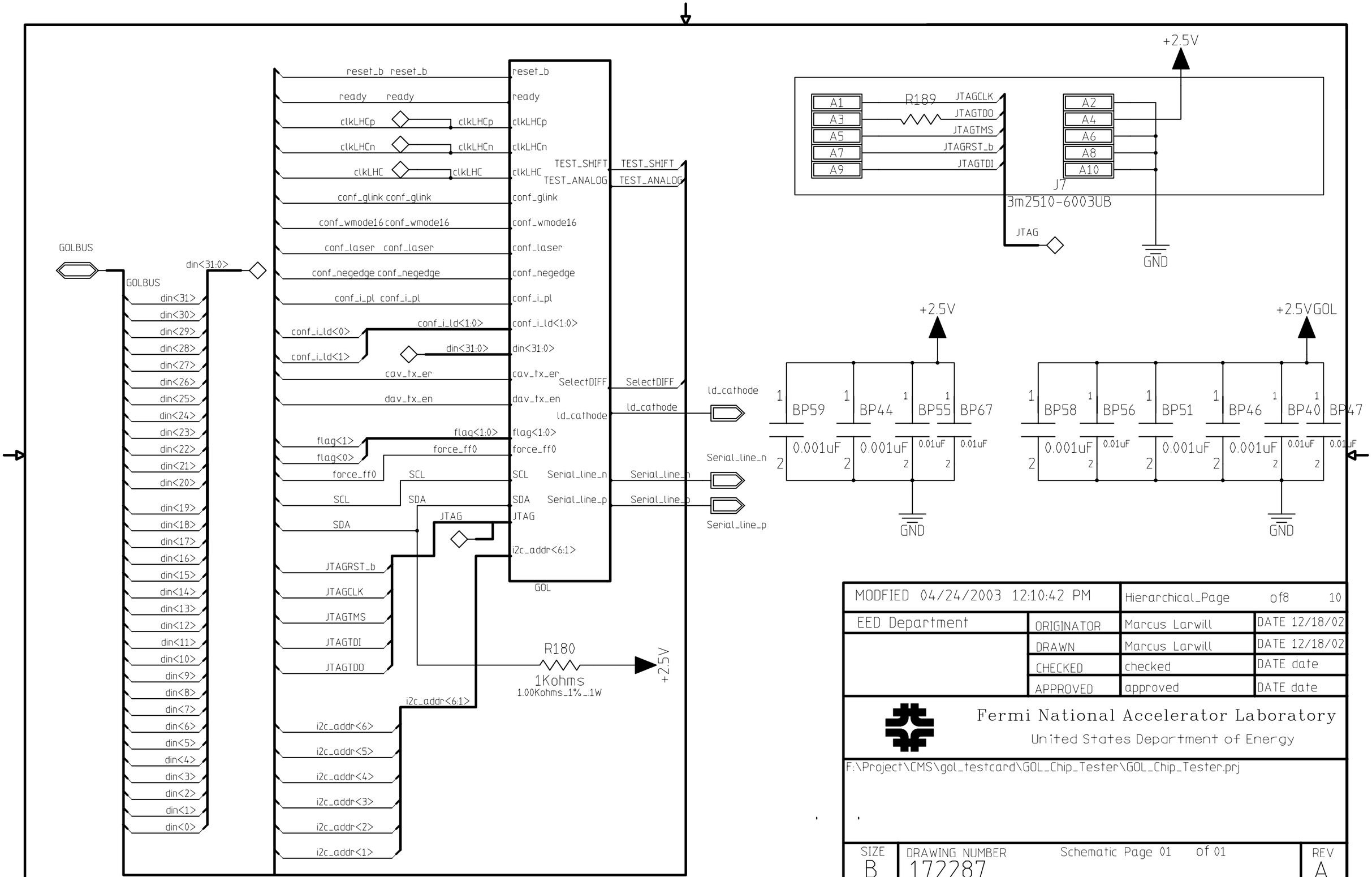
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clkLHCn
clkLHC
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conf_wmode16
conf_laser
conf_negedge
conf_i_pl
conf_i_ld<0>
conf_i_ld<1>
conf_i_ld<1:0>
din<31:0>
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dav_tx_en
flag<1>
flag<0>
flag<1:0>
force_ff0
SCL
SDA
JTAG
JTAGRST_b
JTAGCLK
JTAGTMS
JTAGTDI
JTAGTDO
i2c_addr<6:1>
i2c_addr<6>
i2c_addr<5>
i2c_addr<4>
i2c_addr<3>
i2c_addr<2>
i2c_addr<1>
TEST_SHIFT
TEST_ANALOG
SelectDIFF
ld_cathode
Serial_line_n
Serial_line_p
GOL
R180
1Kohms
1.00Kohms_1%_1W
+2.5V
R189
A1
A3
A5
A7
A9
A2
A4
A6
A8
A10
J7
3m2510-6003UB
GND
BP59
BP44
BP55
BP67
BP58
BP56
BP51
BP46
BP40
BP47
0.001uF
0.01uF
+2.5VGOL
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Hierarchical_Page of8 10
EED Department
ORIGINATOR Marcus Larwill DATE 12/18/02
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REV A
H=2LA; V=2LA