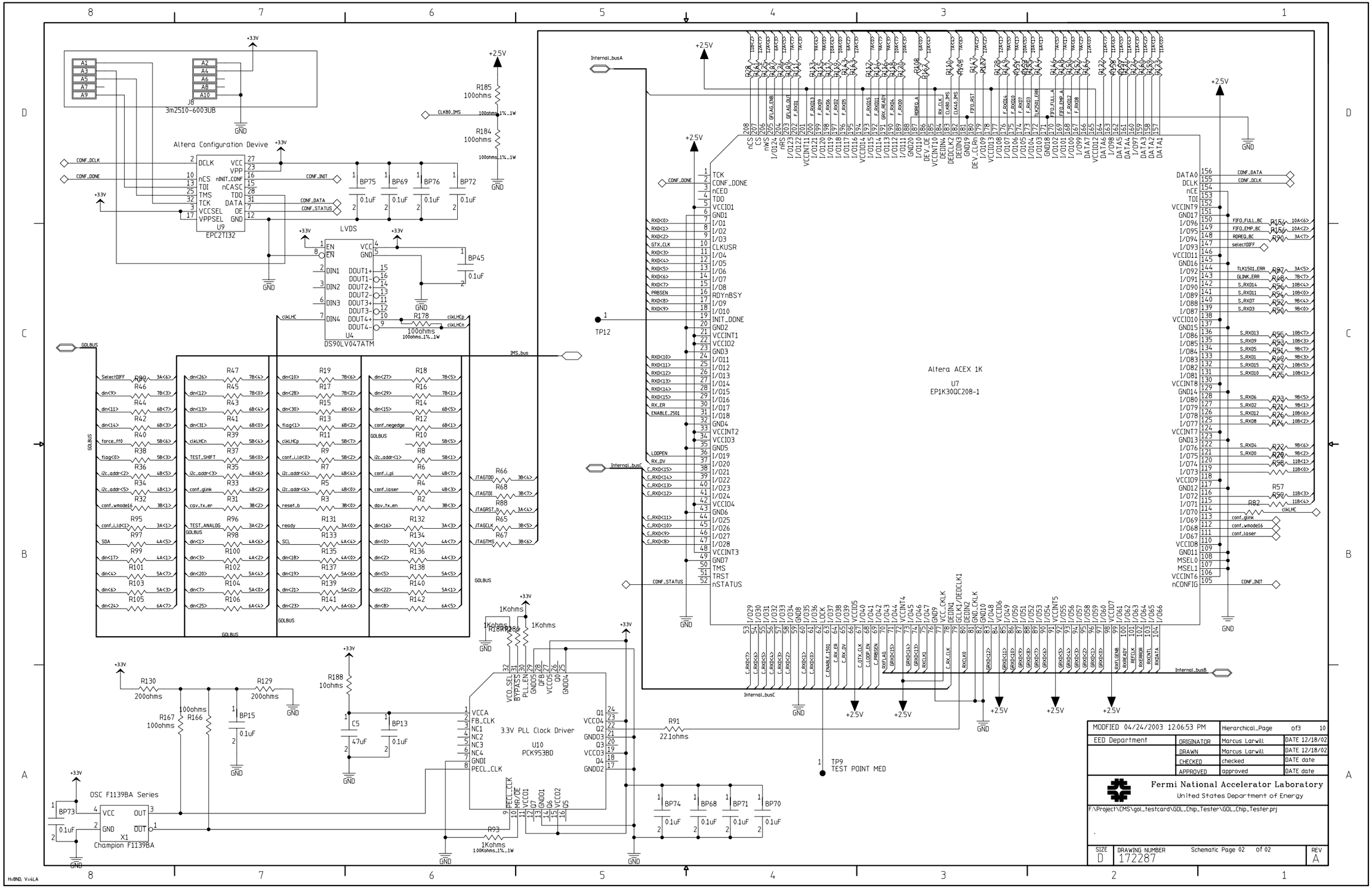
Altera Configuration Devive
EPC2T132
U9
J8
3m2510-6003UB
LVDS
DS90LV047ATM
U4
Altera ACEX 1K
U7
EP1K30QC208-1
3.3V PLL Clock Driver
U10
PCK953BD
OSC F1139BA Series
Champion F1139BA
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TP12
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Hierarchical_Page 3of3 10
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